COMPLIANT

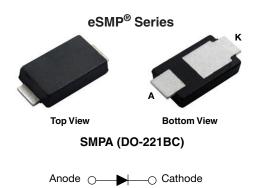
HALOGEN

FREE



Vishay General Semiconductor

Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



LINKS TO ADDITIONAL RESOURCES



| PRIMARY CHARACTERISTICS | | | | |
|---|-----------------|--|--|--|
| I _{F(AV)} | 4.0 A | | | |
| V _{RRM} | 50 V | | | |
| I _{FSM} | 80 A | | | |
| V_F at $I_F = 4.0 \text{ A} (T_A = 125 ^{\circ}\text{C})$ | 0.46 V | | | |
| T _J max. | 150 °C | | | |
| Package | SMPA (DO-221BC) | | | |
| Circuit configuration | Single | | | |

FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPA (DO-221BC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** color band denotes cathode end

| MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted) | | | | |
|---|-----------------------------------|-------------|------|--|
| PARAMETER | SYMBOL | V4PAN50 | UNIT | |
| Device marking code | | 4N5 | | |
| Maximum repetitive peak reverse voltage | V _{RRM} | 50 | V | |
| Maximum DC forward current | I _F ⁽¹⁾ | 4.0 | Α | |
| | I _F ⁽²⁾ | 3.0 | | |
| Maximum DC reverse voltage | V _{DC} | 35 | V | |
| Peak forward surge current 10 ms single half sine-wave superimposed on rated load | I _{FSM} | 80 | А | |
| Operating junction and storage temperature range | T _J , T _{STG} | -40 to +150 | °C | |

Notes

- (1) Units mounted on 15 mm x 15 mm pad areas, 2 oz. PCB
- (2) Free air, mounted on recommended copper pad area



Vishay General Semiconductor

| ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | | | |
|---|------------------------|---|-------------------------------|------|------|------|
| PARAMETER | TEST CONDITIONS | | SYMBOL | TYP. | MAX. | UNIT |
| Instantaneous forward voltage | I _F = 2.0 A | T _A = 25 °C | V _F ⁽¹⁾ | 0.43 | - | V |
| | I _F = 4.0 A | | | 0.51 | 0.59 | |
| | I _F = 2.0 A | T _A = 125 °C | | 0.34 | - | |
| | I _F = 4.0 A | | | 0.46 | 0.54 | |
| Reverse current | V _R = 35 V | T _A = 25 °C T _A = 125 °C | I _R ⁽²⁾ | 8 | - | μA |
| | V _R = 33 V | T _A = 125 °C | | 8.8 | - | mA |
| | V _R = 50 V | T _A = 25 °C | | - | 600 | μA |
| | v _R = 50 v | T _A = 125 °C | | 12 | 35 | mA |
| Typical junction capacitance | 4.0 V, 1 MH | 4.0 V, 1 MHz | | 480 | - | pF |

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

| THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified) | | | |
|---|----------------------|---------|------|
| PARAMETER | SYMBOL | V4PAN50 | UNIT |
| Typical thermal resistance | R _{0JA} (1) | 100 | °C/W |
| | R _{0JM} (1) | 9 |] |

Note

(1) Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient; $R_{\theta JM}$ - junction to mount

| ORDERING INFORMATION (Example) | | | | | |
|--------------------------------|-----------------|------------------------|---------------|------------------------------------|--|
| PREFERRED P/N | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE | |
| V4PAN50-M3/I | 0.032 | I | 14 000 | 13" diameter plastic tape and reel | |

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise specified)

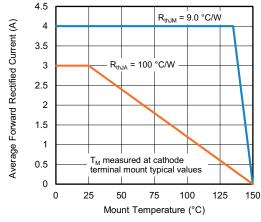


Fig. 1 - Maximum Forward Current Derating Curve

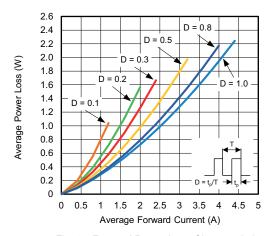


Fig. 2 - Forward Power Loss Characteristics



Vishay General Semiconductor

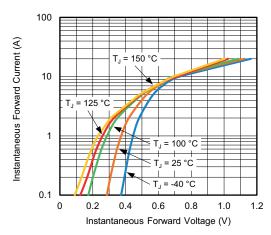


Fig. 3 - Typical Instantaneous Forward Characteristics

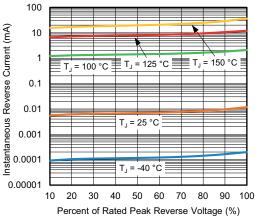


Fig. 4 - Typical Reverse Leakage Characteristics

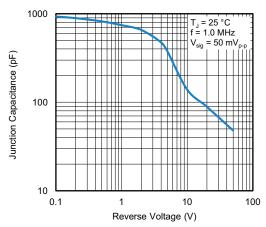


Fig. 5 - Typical Junction Capacitance

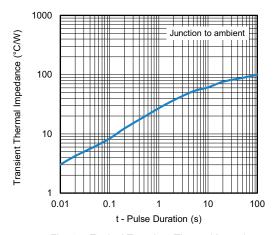


Fig. 6 - Typical Transient Thermal Impedance

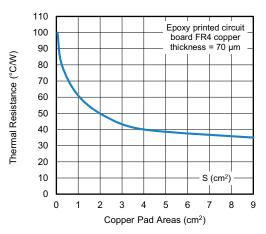


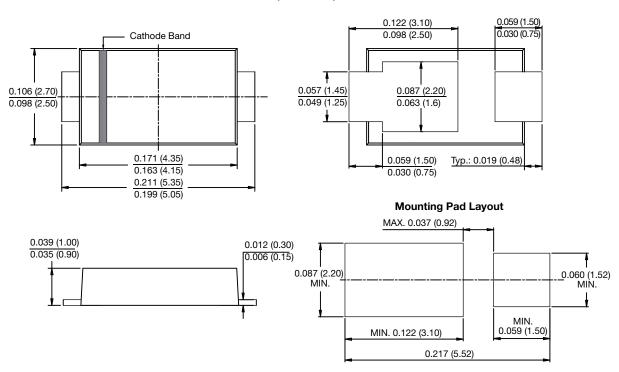
Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas



Vishay General Semiconductor

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMPA (DO-221BC)





Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.