## Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.51$  V at  $I_F = 5.0$  A



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PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 10 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	120 A			
$V_F$ at $I_F$ = 10 A ( $T_A$ = 125 °C)	0.63 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Diode variation	Common cathode			

## FEATURES

- Trench MOS Schottky technology
- · Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

## **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** As marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V20D100C	UNIT	
Device marking code			V20D100C		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub> 100		V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	20	٨	
	per diode		10	- A	
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I <sub>FSM</sub>	120	А	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C	
Storage temperature range		T <sub>STG</sub>	-55 to +150		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

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RoHS

COMPLIANT

V20D100C





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.56	-	- V	
	I <sub>F</sub> = 10 A			0.71	0.79		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.51	-		
	I <sub>F</sub> = 10 A			0.63	0.71		
Reverse current at rated $V_R$ per diode	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	mA	
		T <sub>A</sub> = 125 °C		4	-		
	V 100.V	T <sub>A</sub> = 25 °C		-	0.3		
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 125 °C		9	20		
Typical junction capacitance	4.0 V,	4.0 V, 1 MHz		900	-	pF	

### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V20D100C	UNIT		
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	1.8	°C/W		
	R <sub>0JA</sub> <sup>(2)(3)</sup>	48	0/10		

### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub> $\theta$ JA</sub>

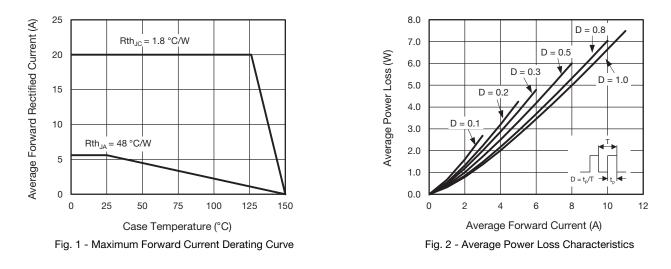
<sup>(3)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20D100C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20D100CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel		

Note

<sup>(1)</sup> AEC-Q101 qualified

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)



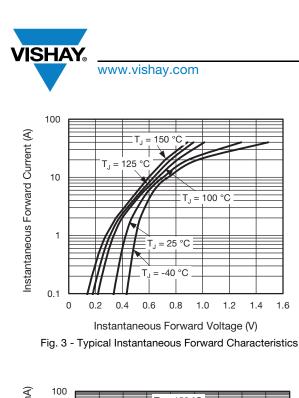
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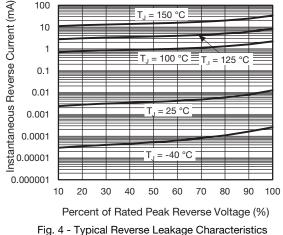
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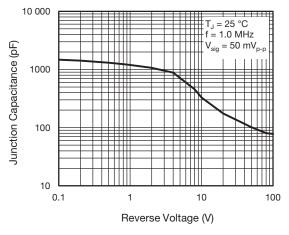
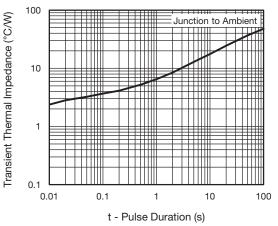
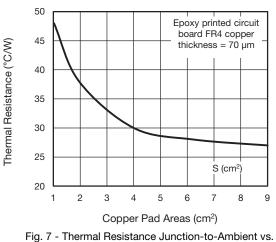


Fig. 5 - Typical Junction Capacitance







**Copper Pad Areas** 

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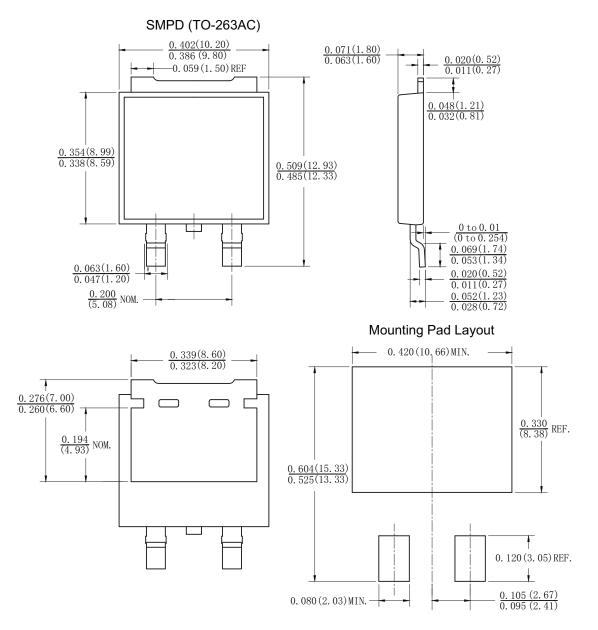
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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