

vPolyTanTM Solid Tantalum Surface Mount Chip Capacitors, Molded Case, High Performance Polymer Type



PERFORMANCE / ELECTRICAL CHARACTERISTICS

Operating Temperature: -55 °C to +105 °C Capacitance Range: $3.3~\mu F$ to $470~\mu F$ Capacitance Tolerance: $\pm~20~\%$ Voltage Rating: $2.5~V_{DC}$ to $35~V_{DC}$

FEATURES

- Ultra-low ESR
- Molded case available in 7 case codes
- Terminations: cases J, P: 100 % tin case A: 100 % tin or Ni/Pd/Au cases T, B, V, D: Ni/Pd/Au

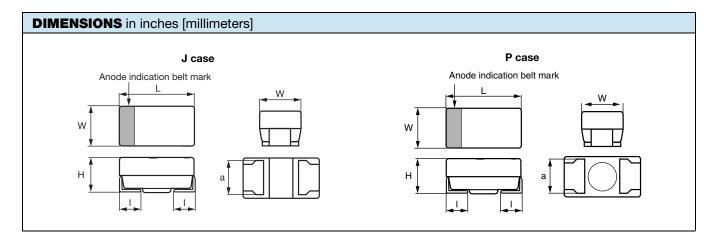


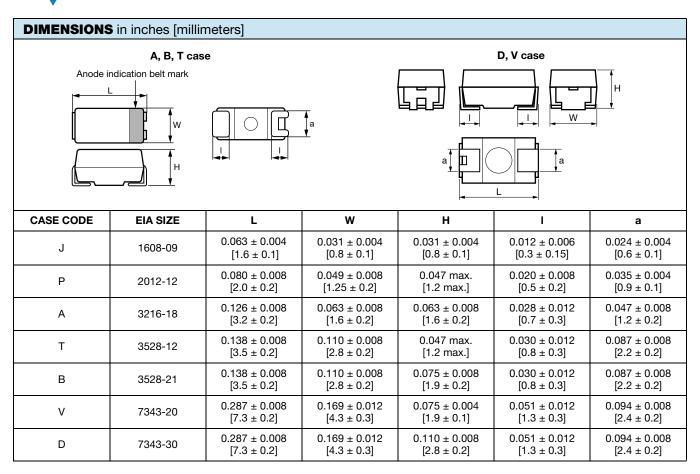
- Compatible with "high volume" automatic pick and place equipment
- Moisture sensitivity level 3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Decoupling, smoothing, filtering
- Bulk energy storage in wireless cards
- Infrastructure equipment
- Storage and networking
- · Computer motherboards
- · Smartphones and tablets

ORDER	ING INFO	ORMATION				
T55	В	156	М	6R3	С	0500
TYPE	CASE CODE L See Ratings and Case Codes table	This is expressed in picofarads. The first two digits are the significant figures. The third is the number of zeros to follow.	CAPACITANCE TOLERANCE L M = ± 20 %	DC VOLTAGE RATING L This is expressed in volts. To complete the three-digit block, zeros precede the voltage rating. A decimal point is indicated by an "R" (6R3 = 6.3 V)	TERMINATION / PACKAGING L C = lead (Pb)-free solderable coating, 7" reel	ESR





RATINGS	AND CASE	CODES						
μF	2.5 V	4.0 V	6.3 V	10 V	16 V	20 V	25 V	35 V
3.3			J/P	J/P				
4.7			J/P/A	P/A				
6.8			P/A	Α	В		В	В
10		J/P/A	P/A	А	В		В	
15		P/A	Α	Α	В	В	B ⁽¹⁾	
22	А	A/B	A/T/B	A/T/B	В			
33	А	A/B	A/T/B	A/T/B				
47	А	A/T/B	A/T/B	В				
68	A/B	A/T/B	T/B	V				
100	A/T/B	A/T/B	A/T/B	D	V			
150	В	B/V	B/V	V				
220	B/V	B/V/D	B/V/D	V ⁽¹⁾ / D				
330	B/V	D/V	V/D	D				
470	B/D	D	V (1)					

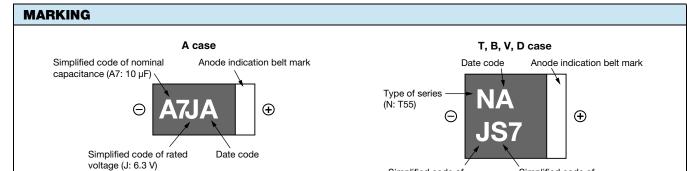
Note

⁽¹⁾ In development.

Simplified code of

nominal capacitance (S7: 47 µF)





Simplified code of

rated voltage (J: 6.3 V)

CAPACITANCE CODE **VOLTAGE CODE** ٧ CODE CAP, µF CODE 2.5 N6 3.3 е G S6 4 4.7 6.3 6.8 W6 J 10 Α7 Α 10 С 16 15 E7 20 D 22 J7 25 Ε 33 N7 35 ٧ 47 S7 W7 68 100 Α8 150 E8 220 J8 330 N8 470 S8

J, P case Simplified code of nominal capacitance (S: 4.7 μF) Simplified code of rated voltage (J: 6.3 V)

RATED VOLTAGE	CAPACITANCE	MARKING
4	10	AG
4	15	EG
6.3	3.3	NJ
6.3	4.7	SJ
6.3	6.8	WJ
6.3	10	AJ
10	3.3	NA
10	4.7	SA

DATE	DATE CODE											
YEAR						MO	NTH					
TEAR	1	2	3	4	5	6	7	8	9	10	11	12
2013	Α	В	С	D	Е	F	G	Н	J	K	L	М
2014	N	Р	Q	R	S	Т	U	V	W	Х	Υ	Z
2015	а	b	С	d	е	f	g	h	j	k		m
2016	n	р	q	r	S	t	u	٧	W	Х	у	Z

Revision: 16-Nov-15 3 Document Number: 40174

For technical questions, contact: polytech@vishav.com



CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μA)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		2.5 Vpc A	AT +105 °C	(/0)	(11152)	
22	Α	T55A226M2R5C0200	5.5	10	200	0.75
22	Α	T55A226M2R5C0180	5.5	10	180	0.79
33	Α	T55A336M2R5C0200	8.2	10	200	0.75
33	Α	T55A336M2R5C0180	8.2	10	180	0.79
47	Α	T55A476M2R5C0200	11.7	10	200	0.75
47	A	T55A476M2R5C0180	11.7	10	180	0.79
68	A	T55A686M2R5C0250	17.0	10	250	0.67
68	A	T55A686M2R5C0200	17.0	10	200	0.75
68	В	T55B686M2R5C0070	17.0	8	70	1.36
100	A	T55A107M2R5C0200	25.0	10	200	0.75
100	A	T55A107M2R5C0100	25.0	10	100	1.07
100	T	T55T107M2R5C0070	25.0	10	70	1.22
100	В	T55B107M2R5C0070	25.0	8	70	1.36
100	В	T55B107M2R5C0076	25.0	8	55	1.53
150	В	T55B157M2R5C0033	37.5	8	70	1.36
150	В	T55B157M2R5C0070	37.5	8	70 55	1.53
220	В	T55B227M2R5C0033	57.5 55.0	8	70	1.36
220	В	T55B227M2R5C0070	55.0	8	70 55	1.53
220	В	T55B227M2R5C0035	55.0	8	45	1.69
220	В	T55B227M2R5C0045	55.0	8	45 35	1.93
	В		55.0 55.0			
220	V	T55B227M2R5C0030		8	30	2.08
220	V V (1)	T55V227M2R5C0025	55.0	10	25	2.73
220	V (1)	T55V227M2R5C0018	55.0	10	18	3.22
220	·	T55V227M2R5C0015	55.0	10	15	3.53
330	В	T55B337M2R5C0070	82.5	8	70	1.36
330	В	T55B337M2R5C0045	82.5	8	45	1.70
330	В	T55B337M2R5C0035	82.5	8	35	1.93
330	В	T55B337M2R5C0025	82.5	8	25	2.28
330	V	T55V337M2R5C0040	82.5	10	40	2.16
330	V	T55V337M2R5C0025	82.5	10	25	2.73
330	V (1)	T55V337M2R5C0018	82.5	10	18	3.22
330	V ⁽¹⁾	T55V337M2R5C0015	82.5	10	15	3.53
470	B	T55B477M2R5C0025	117.5	8	25	2.28
470	D	T55D477M2R5C0050	117.5	10	50	2.12
470	D	T55D477M2R5C0040	117.5	10	40	2.37
470	D	T55D477M2R5C0025	117.5	10	25	3.00
			T +105 °C			
10	J	T55J106M004C0500	10.0	10	500	0.32
10	Р	T55P106M004C0500	5.0	10	500	0.36
10	Р	T55P106M004C0300	5.0	10	300	0.46
10	Р	T55P106M004C0200	5.0	10	200	0.56
10	Α	T55A106M004C0500	4.0	10	500	0.48
10	Α	T55A106M004C0200	4.0	10	200	0.76
15	Р	T55P156M004C0500	10.0	10	500	0.36
15	Р	T55P156M004C0200	10.0	10	200	0.56
15	Α	T55A156M004C0500	6.0	10	500	0.48
15	Α	T55A156M004C0300	6.0	10	300	0.61
15	Α	T55A156M004C0200	6.0	10	200	0.76

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.



				MAY DE	MAY FCD	
CAPACITANCE (µF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		4 V _{DC} A	T +105 °C	(70)	(11132)	
22	A	T55A226M004C0500	8.8	10	500	0.48
22	A	T55A226M004C0300	8.8	10	300	0.61
22	A	T55A226M004C0200	8.8	10	200	0.76
22	В	T55B226M004C0150	8.8	8	150	0.93
22	В	T55B226M004C0200	8.8	8	200	0.81
33	Ā	T55A336M004C0500	13.2	10	500	0.48
33	Α	T55A336M004C0300	13.2	10	300	0.61
33	Α	T55A336M004C0200	13.2	10	200	0.76
33	В	T55B336M004C0200	13.2	8	200	0.81
47	Α	T55A476M004C0500	18.8	10	500	0.48
47	Α	T55A476M004C0200	18.8	10	200	0.76
47	Α	T55A476M004C0180	18.8	10	180	0.79
47	Т	T55T476M004C0200	18.8	10	200	0.72
47	Т	T55T476M004C0080	18.8	10	80	1.15
47	Т	T55T476M004C0070	18.8	10	70	1.22
47	В	T55B476M004C0150	18.8	8	150	0.93
47	В	T55B476M004C0070	18.8	8	70	1.36
68	Α	T55A686M004C0500	27.2	10	500	0.48
68	Α	T55A686M004C0250	27.2	10	250	0.67
68	Α	T55A686M004C0200	27.2	10	200	0.76
68	T	T55T686M004C0200	27.2	10	200	0.72
68	T	T55T686M004C0180	27.2	10	180	0.76
68	T	T55T686M004C0080	27.2	10	80	1.15
68	T	T55T686M004C0070	27.2	10	70	1.22
68	В	T55B686M004C0150	27.2	8	150	0.93
68	В	T55B686M004C0070	27.2	8	70	1.36
100	Α	T55A107M004C0200	40.0	10	200	0.75
100	Α	T55A107M004C0100	40.0	10	100	1.07
100	T	T55T107M004C0150	40.0	10	150	0.84
100	T	T55T107M004C0070	40.0	10	70	1.22
100	В	T55B107M004C0070	40.0	8	70	1.36
100	В	T55B107M004C0055	40.0	8	55	1.53
100	В	T55B107M004C0045	40.0	8	45	1.70
100	В	T55B107M004C0040	40.0	8	40	1.80
100	В	T55B107M004C0035	40.0	8	35	1.92
150	В	T55B157M004C0070	60.0	8	70	1.36
150	В	T55B157M004C0055	60.0	8	55	1.53
150	В	T55B157M004C0045	60.0	8	45	1.69
150	В	T55B157M004C0040	60.0	8	40	1.80
150	В	T55B157M004C0035	60.0	8	35	1.93
150	V	T55V157M004C0045	60.0	10	45 05	2.03
150	V	T55V157M004C0025	60.0	10	25 70	2.73
220	В	T55B227M004C0070	88.0	8	70 60	1.36
220	В	T55B227M004C0060	88.0	8	60 55	1.47
220	В	T55B227M004C0055	88.0	8	55 45	1.53
220	В	T55B227M004C0045	88.0	8	45 25	1.70
220	В	T55B227M004C0035	88.0	8	35 55	1.93
220	V	T55V227M004C0055	88.0	10	55 45	1.84
220	V	T55V227M004C0045	88.0	10	45 40	2.03
220	V	T55V227M004C0040	88.0	10	40 25	2.16
220	V	T55V227M004C0035	88.0	10 10	35 25	2.31
220	V V ⁽¹⁾	T55V227M004C0025	88.0	10	25	2.73
220 220	V (1)	T55V227M004C0018 T55V227M004C0015	88.0 88.0	10 10	18 15	3.22 3.53
220 220	V (1)	T55V227M004C0015	88.0 88.0	10	55	3.53 2.02
220 220	D D	T55D227M004C0055	88.0 88.0	10	55 40	2.02
220	D	T55D227M004C0040	88.0	10	40 25	3.00

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.



CAPACITANCE (µF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		4 Vpc A	T +105 °C	(/0)	(11152)	
330	D	T55D337M004C0050	132.0	10	50	2.12
330	D	T55D337M004C0040	132.0	10	40	2.37
330	D	T55D337M004C0025	132.0	10	25	3.00
330	D (1)	T55D337M004C0015	132.0	10	15	3.87
330	V	T55V337M004C0050	132.0	10	50	1.93
330	V	T55V337M004C0045	132.0	10	45	2.03
330	V	T55V337M004C0040	132.0	10	40	2.16
330	V	T55V337M004C0025	132.0	10	25	2.73
330	V (1)	T55V337M004C0018	132.0	10	18	3.22
330	V (1)	T55V337M004C0015	132.0	10	15	3.53
470	D	T55D477M004C00T5	188.0	10	55	2.02
470 470	D	T55D477M004C0050	188.0	10	50	2.12
470 470	D		188.0	10	40	2.12
470 470	D	T55D477M004C0040		10	40 25	3.00
	D (1)	T55D477M004C0025	188.0			
470	טייט	T55D477M004C0018	188.0	10	18	3.53
			AT +105 °C		500	0.00
3.3	J	T55J335M6R3C0500	10.0	10	500	0.32
3.3	P	T55P335M6R3C0500	5.0	10	500	0.36
4.7	J	T55J475M6R3C0500	10.0	10	500	0.32
4.7	Р	T55P475M6R3C0500	5.0	10	500	0.36
4.7	Α	T55A475M6R3C0500	3.0	10	500	0.48
6.8	Р	T55P685M6R3C0500	5.0	10	500	0.36
6.8	Α	T55A685M6R3C0500	4.2	10	500	0.48
10	Р	T55P106M6R3C0500	10.0	10	500	0.36
10	Р	T55P106M6R3C0200	10.0	10	200	0.56
10	Α	T55A106M6R3C0500	6.3	10	500	0.48
10	Α	T55A106M6R3C0300	6.3	10	300	0.61
10	Α	T55A106M6R3C0200	6.3	10	200	0.76
15	Α	T55A156M6R3C0500	9.4	10	500	0.48
15	Α	T55A156M6R3C0300	9.4	10	300	0.61
15	Α	T55A156M6R3C0200	9.4	10	200	0.76
22	Α	T55A226M6R3C0500	13.8	10	500	0.48
22	Α	T55A226M6R3C0300	13.8	10	300	0.61
22	Α	T55A226M6R3C0200	13.8	10	200	0.76
22	T	T55T226M6R3C0150	13.8	10	150	0.84
22	Т	T55T226M6R3C0070	13.8	10	70	1.22
22	В	T55B226M6R3C0150	13.8	8	150	0.93
33	Α	T55A336M6R3C0500	20.7	10	500	0.48
33	Α	T55A336M6R3C0200	20.7	10	200	0.76
33	Α	T55A336M6R3C0180	20.7	10	180	0.79
33	T	T55T336M6R3C0200	20.7	10	200	0.72
33	T	T55T336M6R3C0150	20.7	10	150	0.84
33	T	T55T336M6R3C0070	20.7	10	70	1.22
33	В	T55B336M6R3C0200	20.7	8	200	0.81
33	В	T55B336M6R3C0150	20.7	8	150	0.93
33	В	T55B336M6R3C0080	20.7	8	80	1.27
33	В	T55B336M6R3C0070	20.7	8	70	1.36
33	В	T55B336M6R3C0040	20.7	8	40	1.80

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.



CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μA)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		6.3 V _{DC} A	AT +105 °C	(,,,	()	
47	А	T55A476M6R3C0500	29.6	10	500	0.48
47	Α	T55A476M6R3C0200	29.6	10	200	0.76
47	Α	T55A476M6R3C0180	29.6	10	180	0.79
47	Α	T55A476M6R3C0150	29.6	10	150	0.88
47	Α	T55A476M6R3C0100	29.6	10	100	1.07
47	Α	T55A476M6R3C0070	29.6	10	70	1.28
47	Т	T55T476M6R3C0200	29.6	10	200	0.72
47	Т	T55T476M6R3C0120	29.6	10	120	0.93
47	Т	T55T476M6R3C0080	29.6	10	80	1.15
47	Т	T55T476M6R3C0070	29.6	10	70	1.22
47	В	T55B476M6R3C0150	29.6	8	150	0.93
47	В	T55B476M6R3C0070	29.6	8	70	1.36
47	В	T55B476M6R3C0060	29.6	8	60	1.47
47	В	T55B476M6R3C0040	29.6	8	40	1.80
68	Т	T55T686M6R3C0200	42.8	10	200	0.72
68	Т	T55T686M6R3C0150	42.8	10	150	0.83
68	Т	T55T686M6R3C0070	42.8	10	70	1.22
68	В	T55B686M6R3C0150	42.8	8	150	0.93
68	В	T55B686M6R3C0070	42.8	8	70	1.36
100	Α	T55A107M6R3C0200	63.0	10	200	0.76
100	Α	T55A107M6R3C0150	63.0	10	150	0.88
100	Α	T55A107M6R3C0100	63.0	10	100	1.07
100	Α	T55A107M6R3C0070	63.0	10	70	1.28
100	Т	T55T107M6R3C0200	63.0	10	200	0.72
100	Т	T55T107M6R3C0070	63.0	10	70	1.22
100	В	T55B107M6R3C0100	63.0	8	100	1.14
100	В	T55B107M6R3C0070	63.0	8	70	1.36
100	В	T55B107M6R3C0055	63.0	8	55	1.53
100	В	T55B107M6R3C0045	63.0	8	45	1.70
100	В	T55B107M6R3C0040	63.0	8	40	1.80
100	В	T55B107M6R3C0035	63.0	8	35	1.93
150	В	T55B157M6R3C0100	94.5	8	100	1.14
150	В	T55B157M6R3C0070	94.5	8	70	1.36
150	В	T55B157M6R3C0055	94.5	8	55	1.53
150	В	T55B157M6R3C0045	94.5	8	45	1.70
150	В	T55B157M6R3C0040	94.5	8	40	1.80
150	В	T55B157M6R3C0035	94.5	8	35	1.93
150	B ⁽¹⁾	T55B157M6R3C0025	94.5	8	25	2.28
150	V	T55V157M6R3C0055	94.5	10	55	1.84
150	V	T55V157M6R3C0045	94.5	10	45	2.03
150	V	T55V157M6R3C0040	94.5	10	40	2.16
150	V	T55V157M6R3C0035	94.5	10	35	2.31
150	V	T55V157M6R3C0025	94.5	10	25	2.73

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.





CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz	MAX. ESR AT + 25 °C 100 kHz	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		COV		(%)	(mΩ)	
000			AT +105 °C	0	000	0.00
220	В	T55B227M6R3C0200	138.6	8	200	0.80
220	В	T55B227M6R3C0070	138.6	8	70	1.36
220	В	T55B227M6R3C0045	138.6	8	45	1.69
220	В	T55B227M6R3C0035	138.6	8	35	1.93
220	В	T55B227M6R3C0025	138.6	8	25	2.28
220	V	T55V227M6R3C0050	138.6	10	50	1.93
220	V	T55V227M6R3C0045	138.6	10	45	2.03
220	V	T55V227M6R3C0040	138.6	10	40	2.16
220	V	T55V227M6R3C0035	138.6	10	35	2.31
220	V	T55V227M6R3C0025	138.6	10	25	2.73
220	V (1)	T55V227M6R3C0018	138.6	10	18	3.22
220	V ⁽¹⁾	T55V227M6R3C0015	138.6	10	15	3.53
220	D	T55D227M6R3C0055	138.6	10	55	2.02
220	D	T55D227M6R3C0050	138.6	10	50	2.12
220	D	T55D227M6R3C0040	138.6	10	40	2.37
220	D	T55D227M6R3C0035	138.6	10	35	2.53
220	D	T55D227M6R3C0025	138.6	10	25	3.00
330	V	T55V337M6R3C0050	207.9	10	50	1.93
330	V	T55V337M6R3C0045	207.9	10	45	2.03
330	V	T55V337M6R3C0040	207.9	10	40	2.16
330	V	T55V337M6R3C0025	207.9	10	25	2.73
330	V ⁽¹⁾	T55V337M6R3C0018	207.9	10	18	3.22
330	V ⁽¹⁾	T55V337M6R3C0015	207.9	10	15	3.53
330	D	T55D337M6R3C0055	207.9	10	55	2.02
330	D	T55D337M6R3C0050	207.9	10	50	2.12
330	D	T55D337M6R3C0045	207.9	10	45	2.23
330	D	T55D337M6R3C0040	207.9	10	40	2.37
330	D	T55D337M6R3C0025	207.9	10	25	3.00
330	D ⁽¹⁾	T55D337M6R3C0018	207.9	10	18	3.53
330	D (1)	T55D337M6R3C0015	207.9	10	15	3.87
330	D ⁽¹⁾	T55D337M6R3C0012	207.9	10	12	4.33
470	V (1)	T55V477M6R3C0055	296.1	10	55	1.84
470	V (1)	T55V477M6R3C0050	296.1	10	50	1.93
410	V . ,		T +105 °C	10	30	1.50
3.3	J	T55J335M010C0500	10.0	10	500	0.32
3.3	P P	T55P335M010C0500	5.0 10.0	10 10	500 500	0.36
4.7		T55P475M010C0500	10.0	10	500	0.36
4.7	P	T55P475M010C0200	10.0	10	200	0.56
4.7	A	T55A475M010C0500	4.7	10	500	0.48
4.7	A	T55A475M010C0300	4.7	10	300	0.61
4.7	A	T55A475M010C0200	4.7	10	200	0.76
6.8	A	T55A685M010C0500	6.8	10	500	0.48
6.8	A	T55A685M010C0300	6.8	10	300	0.61
6.8	Α	T55A685M010C0200	6.8	10	200	0.76

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.





CAPACITANCE (µF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		10 Vpc A	AT +105 °C	(/0)	(11152)	
10	Α	T55A106M010C0500	10.0	10	500	0.48
10	A	T55A106M010C0300	10.0	10	300	0.61
10	A	T55A106M010C0200	10.0	10	200	0.76
15	A	T55A156M010C0500	15.0	10	500	0.48
15	A	T55A156M010C0200	15.0	10	200	0.76
22	A	T55A226M010C0500	22.0	10	500	0.48
22	A	T55A226M010C0200	22.0	10	200	0.76
22	T	T55T226M010C0200	22.0	10	200	0.72
22	Ť	T55T226M010C0150	22.0	10	150	0.84
22	T.	T55T226M010C0070	22.0	10	70	1.22
22	В	T55B226M010C0300	22.0	8	300	0.66
22	В	T55B226M010C0200	22.0	8	200	0.81
22	В	T55B226M010C0150	22.0	8	150	0.93
22	В	T55B226M010C0070	22.0	8	70	1.36
33	A	T55A336M010C0500	33.0	10	500	0.48
33	A	T55A336M010C0300	33.0	10	150	0.48
33	A		33.0	10	70	1.28
33		T55A336M010C0070	33.0		200	
	A	T55A336M010C0200		10		0.76
33	T	T55T336M010C0200	33.0	10	200	0.72
33	T T	T55T336M010C0150	33.0	10	150	0.84
33	T T	T55T336M010C0080	33.0	10	80	1.14
33	T	T55T336M010C0070	33.0	10	70	1.22
33	В	T55B336M010C0200	33.0	8	200	0.81
33	В	T55B336M010C0150	33.0	8	150	0.93
33	В	T55B336M010C0080	33.0	8	80	1.27
33	В	T55B336M010C0070	33.0	8	70	1.36
47	В	T55B476M010C0150	47.0	8	150	0.93
47	В	T55B476M010C0070	47.0	8	70	1.36
68	V	T55V686M010C0100	68.0	10	100	1.36
68	V	T55V686M010C0060	68.0	10	60	1.76
100	D	T55D107M010C0080	100.0	10	80	1.67
100	D	T55D107M010C0055	100.0	10	55	2.02
100	D	T55D107M010C0045	100.0	10	45	2.23
100	D	T55D107M010C0025	100.0	10	25	3.00
100	D (1)	T55D107M010C0018	100.0	10	18	3.53
150	V	T55V157M010C0055	150.0	10	55	1.84
150	V	T55V157M010C0045	150.0	10	45	2.03
150	V	T55V157M010C0040	150.0	10	40	2.16
150	V	T55V157M010C0025	150.0	10	25	2.73
150	V ⁽¹⁾	T55V157M010C0015	150.0	10	15	3.53
220	V (1)	T55V227M010C0050	220.0	10	50	1.93
220	V (1)	T55V227M010C0045	220.0	10	45	2.03
220	V (1)	T55V227M010C0040	220.0	10	40	2.16
220	V (1)	T55V227M010C0025	220.0	10	25	2.73
220	V (1)	T55V227M010C0018	220.0	10	18	3.22
220	D	T55D227M010C0055	220.0	10	55	2.02
220	D	T55D227M010C0050	220.0	10	50	2.12
220	D	T55D227M010C0040	220.0	10	40	2.37
220	D	T55D227M010C0025	220.0	10	25	3.00
220	D (1)	T55D227M010C0018	220.0	10	18	3.53
330	D	T55D337M010C0040	330.0	10	40	2.37
330	D	T55D337M010C0035	330.0	10	35	2.53
330	D	T55D337M010C0025	330.0	10	25	3.00

[•] Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.

STANDARD RA	ATINGS					
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT + 25 °C 100 kHz (mΩ)	MAX. RIPPLE, AT 45 °C 100 kHz I _{RMS} (A)
		16 V _{DC} A	T +105 °C			
6.8	В	T55B685M016C0200	10.8	8	200	0.80
10	В	T55B106M016C0200	16.0	8	200	0.80
10	В	T55B106M016C0100	16.0	8	100	1.14
15	В	T55B156M016C0150	24.0	8	150	0.93
15	В	T55B156M016C0090	24.0	8	90	1.20
22	В	T55B226M016C0150	35.2	8	150	0.93
22	В	T55B226M016C0070	35.2	8	70	1.36
100	V	T55V107M016C0050	160.0	10	50	1.93
		20 V _{DC} A	T +105 °C			
15	В	T55B156M020C0090	30.0	8	90	1.20
15	В	T55B156M020C0070	30.0	8	70	1.36
		25 V _{DC} A	T +105 °C			
6.8	В	T55B685M025C0100	17.0	8	100	1.14
10	В	T55B106M025C0150	25.0	8	150	0.93
10	В	T55B106M025C0100	25.0	8	100	1.14
15	B (1)	T55B156M025C0100	37.5	8	100	1.14
		35 V _{DC} A	T +105 °C			
6.8	В	T55B685M035C0200	23.8	8	200	0.81

Notes

• Termination code "C": cases J, P: 100 % tin, case A: 100 % tin or Ni/Pd/Au, cases T, B, V, D: Ni/Pd/Au.

⁽¹⁾ In development.

RECOMMENDED VOLTAGE DERATING GUIDELINES					
CAPACITOR VOLTAGE RATING	OPERATING VOLTAGE				
2.5	2.3				
4.0	3.6				
6.3	5.7				
10	9.0				
16	12.8				
20	16				
25	20				
35	28				

POWER DISSIPATION						
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION (W) AT ≤ +45 °C IN FREE AIR					
J	0.050					
Р	0.064					
A	0.115					
Т	0.105					
В	0.130					
V	0.187					
D	0.225					



STANDARD PACKAGING QUANTITY					
CASE CODE	UNITS PER 7" REEL				
J	4000				
Р	3000				
A	2000				
Т	3000				
В	2000				
V	800				
D	500				

ITEM	ICE CHARACTERISTICS CONDITION									DOST TEST DEDECOMANICE				
I I E IVI	CONDITION									POST TEST PERFORMANCE Specified 55.00 405.00				
											initial value	-55 °C	+105 °C	
										Capacitance change	-	-20 % to 0 %	0 % to 30 %	
Temperature Measure the specified characteristics in each stage						Dissipation factor shown in Standard Ratings table or less	8 to 10	14	-					
					Leakage current	Refer to Standard Ratings table	-	Not more than 1 CV or 30 μA which is greater						
	105 °C, 1000 successive test cycles in series with a 1 k Ω resistor at the rate of 30 s ON, 30 s OFF; tested voltage per table below:								٧,	Capacitance change	Within ± 20 % of initial value			
Surge voltage	Rated voltage	2.5	4	6.3	10	16	20	25	35	Dissipation factor	n factor Within initial limit			
	Surge voltage	3.2	5.2	8.2	13	20	23	29	40	Leakage current	Shall not exceed 300 % of initial limit			
0.11	Reflow b	Reflow board surface peak temperature:								Capacitance change	Within ± 20	Within ± 20 % of initial value		
Solder heat resistance	less than 260 °C									Dissipation factor	Initial specified value or less			
	Time: 5 s max.									Leakage current	Shall not exceed 300 % of initial specified value			
Moisture resistance no load	Leave at 60 °C and 90 % RH for 500 h									Capacitance change	$V_R \le 4 V$	Relative to the v +50 % to -20 %	value before test	
											$V_R \ge 6.3 \text{ V}$	Relative to the v +40 % to -20 %	value before test	
										Dissipation factor	Initial specifi	ed value or less		
										Leakage current	Shall not exceed 300 % of initial specified value			
	105 °C. The rated voltage is applied through a protective resistor of 3 Ω for 1000 h									Capacitance change	Within ± 20 % of initial value			
High temperature load								throu	gn a	Dissipation factor	Initial specified value or less			
										Leakage current	Shall not exceed 300 % of initial specified value			
Thermal shock	Leave at -55 °C, normal temperature, 105 °C,									Capacitance change	Within ± 20 % of initial value or less			
	and normal temperature for 30 min., 15 min. 30 min., and 15 min.									Dissipation factor	Initial specified value or less			
	Repeat this operation 5 times running.									Leakage current	Shall not exceed 300 % of initial specified value			
Failure rate	105 °C. The rated voltage is applied through a protective resistor of 1 Ω /V.						plied	thro	ugh	1 % / 1000 h				

Note

• Test conditions per JIS C5101-1



Guide for Tantalum Solid Electrolyte Chip Capacitors with Polymer Cathode

INTRODUCTION

Tantalum electrolytic capacitors are the preferred choice in applications where volumetric efficiency, stable electrical parameters, high reliability, and long service life are primary considerations. The stability and resistance to elevated temperatures of the tantalum/tantalum oxide/manganese dioxide system make solid tantalum capacitors an appropriate choice for today's surface mount assembly technology.

Vishay Sprague has been a pioneer and leader in this field, producing a large variety of tantalum capacitor types for consumer, industrial, automotive, military, and aerospace electronic applications.

Tantalum is not found in its pure state. Rather, it is commonly found in a number of oxide minerals, often in combination with Columbium ore. This combination is known as "tantalite" when its contents are more than one-half tantalum. Important sources of tantalite include Australia, Brazil, Canada, China, and several African countries. Synthetic tantalite concentrates produced from tin slags in Thailand, Malaysia, and Brazil are also a significant raw material for tantalum production.

Electronic applications, and particularly capacitors, consume the largest share of world tantalum production. Other important applications for tantalum include cutting tools (tantalum carbide), high temperature super alloys, chemical processing equipment, medical implants, and military ordnance.

Vishay Sprague is a major user of tantalum materials in the form of powder and wire for capacitor elements and rod and sheet for high temperature vacuum processing.

THE BASICS OF TANTALUM CAPACITORS

Most metals form crystalline oxides which are non-protecting, such as rust on iron or black oxide on copper. A few metals form dense, stable, tightly adhering, electrically insulating oxides. These are the so-called "valve"metals and include titanium, zirconium, niobium, tantalum, hafnium, and aluminum. Only a few of these permit the accurate control of oxide thickness by electrochemical means. Of these, the most valuable for the electronics industry are aluminum and tantalum.

Capacitors are basic to all kinds of electrical equipment, from radios and television sets to missile controls and automobile ignitions. Their function is to store an electrical charge for later use.

Capacitors consist of two conducting surfaces, usually metal plates, whose function is to conduct electricity. They are separated by an insulating material or dielectric. The dielectric used in all tantalum electrolytic capacitors is tantalum pentoxide.

Tantalum pentoxide compound possesses high-dielectric strength and a high-dielectric constant. As capacitors are being manufactured, a film of tantalum pentoxide is applied to their electrodes by means of an electrolytic process. The film is applied in various thicknesses and at various voltages and although transparent to begin with, it takes on different colors as light refracts through it. This coloring occurs on the tantalum electrodes of all types of tantalum capacitors.

Rating for rating, tantalum capacitors tend to have as much as three times better capacitance/volume efficiency than aluminum electrolytic capacitors. An approximation of the capacitance/volume efficiency of other types of capacitors may be inferred from the following table, which shows the dielectric constant ranges of the various materials used in each type. Note that tantalum pentoxide has a dielectric constant of 26, some three times greater than that of aluminum oxide. This, in addition to the fact that extremely thin films can be deposited during the electrolytic process mentioned earlier, makes the tantalum capacitor extremely efficient with respect to the number of microfarads available per unit volume. The capacitance of any capacitor is determined by the surface area of the two conducting plates, the distance between the plates, and the dielectric constant of the insulating material between the plates.

COMPARISON OF CAPACITOR DIELECTRIC CONSTANTS					
DIELECTRIC	e DIELECTRIC CONSTANT				
Air or vacuum	1.0				
Paper	2.0 to 6.0				
Plastic	2.1 to 6.0				
Mineral oil	2.2 to 2.3				
Silicone oil	2.7 to 2.8				
Quartz	3.8 to 4.4				
Glass	4.8 to 8.0				
Porcelain	5.1 to 5.9				
Mica	5.4 to 8.7				
Aluminum oxide	8.4				
Tantalum pentoxide	26				
Ceramic	12 to 400K				

In the tantalum electrolytic capacitor, the distance between the plates is very small since it is only the thickness of the tantalum pentoxide film. As the dielectric constant of the tantalum pentoxide is high, the capacitance of a tantalum capacitor is high if the area of the plates is large:

$$C = \frac{eA}{t}$$

where

C = capacitance

e = dielectric constant

A = surface area of the dielectric

t = thickness of the dielectric

Tantalum capacitors contain either liquid or solid electrolytes. In solid electrolyte capacitors, a dry material (manganese dioxide) forms the cathode plate. A tantalum lead is embedded in or welded to the pellet, which is in turn connected to a termination or lead wire. The drawings show the construction details of the surface mount types of tantalum capacitors shown in this catalog.



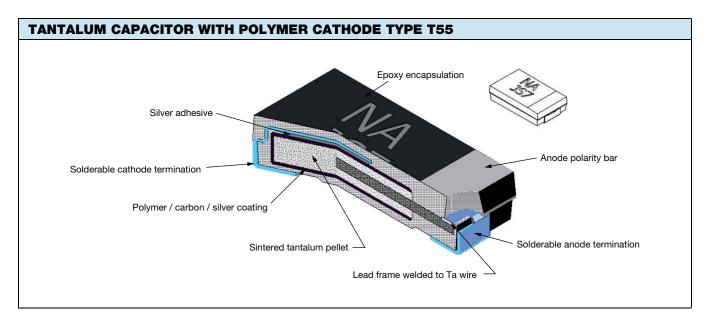
SOLID ELECTROLYTE TANTALUM CAPACITORS

Solid electrolyte polymer capacitors utilizes high performance polymer as cathode system, which is formed on the tantalum pentoxide dielectric layer.

The pellet is then coated with graphite, followed by a layer of metallic silver, which provides a conductive surface between the pellet and the outer termination (leadframe or other).

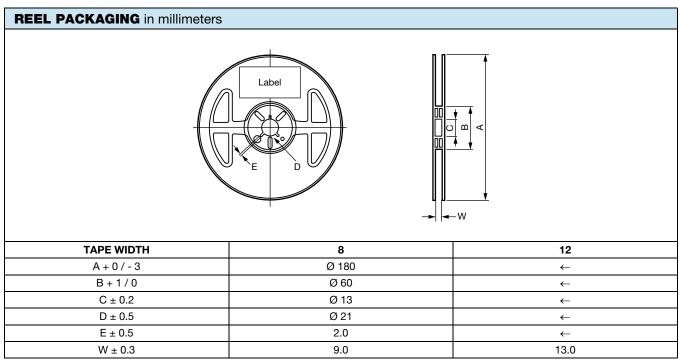
Molded chip polymer tantalum capacitor encases the element in plastic resins, such as epoxy materials. After assembly, the capacitors are tested and inspected to assure long life and reliability. It offers excellent reliability and high stability for consumer and commercial electronics.

Surface mount designs of T55 solid tantalum polymer capacitors use lead frames.



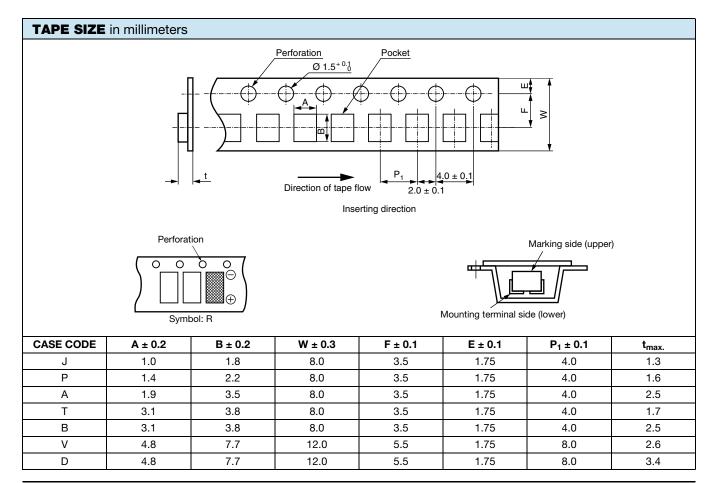
SOLID TANTALUM CAPACITORS - MOLDED CASE						
SERIES	T55					
PRODUCT IMAGE	THE THE PERSON NAMED IN COLUMN TWO IS NOT THE PERSON NAMED IN COLUMN TO THE PERSON NAMED IN COLU					
TYPE	VPolyTan [™] , molded case, high performance polymer					
TEMPERATURE RANGE	Operating temperature: -55 °C to +105 °C					
CASE SIZES	J, P, A, T, B, V, D					
CAPACITANCE RANGE	3.3 μF to 470 μF					
VOLTAGE RANGE	2.5 V to 35 V					
CAPACITANCE TOLERANCE	± 20 %					
DISSIPATION FACTOR	8 % to 10 %					
ESR	15 m Ω to 500 m Ω					
TERMINATION	Cases J, P: 100 % tin Case A: 100 % tin or Ni/Pd/Au Cases T, B, V, D: Ni/Pd/Au					





Note

• A reel diameter of 330 mm is also applicable.



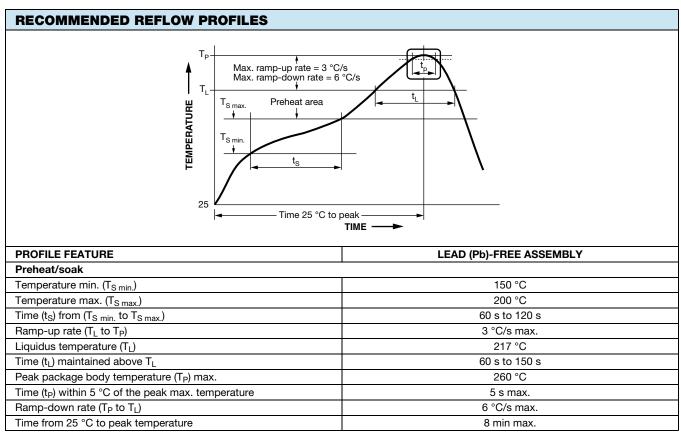
Revision: 08-Oct-15 3 Document Number: 40076



PACKING AND STORAGE

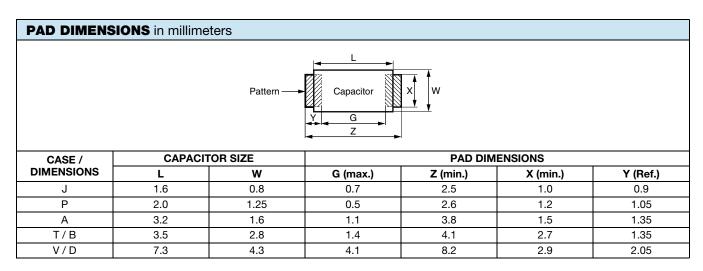
T55 capacitors meet moisture sensitivity level rating (MSL) of 3 as specified in IPC/JEDEC® J-STD-020D.1 and are dry packaged in moisture barrier bags (MBB) per J-STD-033. Level 3 specifies a floor life (out of bag) of 168 hours at 30 °C maximum and 60 % relative humidity (RH). Unused capacitors should be re-sealed in the MMB with fresh desiccant. A moisture strip (humidity indicator card) is included in the bag to assure dryness. To remove excess moisture, capacitors can be dried at 40 °C (standard "dry box" conditions).

For detailed recommendations please refer to J-STD-033.



Note

T55 capacitors are process sensitive. PSL classification to JEDEC J-STD-075: R4G.



GUIDE TO APPLICATION

 AC Ripple Current: the maximum allowable ripple current shall be determined from the formula:

$$I_{RMS} = \sqrt{\frac{P}{R_{ESR}}}$$

where.

P = power dissipation in W at +45 °C as given in the tables in the product datasheets.

R_{ESR} = the capacitor equivalent series resistance at the specified frequency.

2. **AC Ripple Voltage:** the maximum allowable ripple voltage shall be determined from the formula:

$$V_{\text{RMS}} \, = \, Z \sqrt{\frac{P}{R_{\text{ESR}}}}$$

or, from the formula:

$$V_{RMS} = I_{RMS} \times Z$$

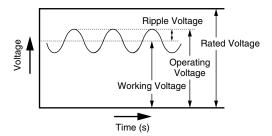
where,

P = power dissipation in W at +45 °C as given in the tables in the product datasheets.

R_{ESR} = The capacitor equivalent series resistance at the specified frequency.

Z = The capacitor impedance at the specified frequency.

2.1 The tantalum capacitors must be used in such a condition that the sum of the working voltage and ripple voltage peak values does not exceed the rated voltage as shown in figure below.



3. **Temperature Derating:** power dissipation is affected by the heat sinking capability of the mounting surface. If these capacitors are to be operated at temperatures above +45 °C, the permissible ripple current (or voltage) shall be calculated using the derating coefficient as shown in the table below:

MAXIMUM RIPPLE CURRENT TEMPERATURE DERATING FACTOR					
≤ 45 °C	1.0				
55 °C	0.8				
85 °C	0.6				
105 °C	0.4				

4. **Reverse Voltage:** these capacitors are capable of withstanding peak voltages in the reverse direction equal to 10 % of the DC rating at +25 °C, 5 % of the DC rating at +25 °C, 5 % of the DC rating at +85 °C, and 1 % of the DC rating at +105 °C.

5. **Mounting Precautions:**

5.1 Limit Pressure on Capacitor Installation with Mounter: pressure must not exceed 4.9 N with a tool end diameter of 1.5 mm when applied to the capacitors using an absorber, centering tweezers, or similar (maximum permitted pressurization time: 5 s). An excessively low absorber setting position would result in not only the application of undue force to the capacitors but capacitor and other component scattering, circuit board wiring breakage, and / or cracking as well, particularly when the capacitors are mounted together with other chips having a height of 1 mm or less.

5.2 Flux Selection

- 5.2.1 Select a flux that contains a minimum of chlorine and amine.
- 5.2.2 After flux use, the chlorine and amine in the flux remain must be removed.
- 5.3 Cleaning After Mounting: the following solvents are usable when cleaning the capacitors after mounting. Never use a highly active solvent.
 - •Halogen organic solvent (HCFC225, etc.)
 - •Alcoholic solvent (IPA, ethanol, etc.)
 - Petroleum solvent, alkali saponifying agent, water, etc.

Circuit board cleaning must be conducted at a temperature of not higher than 50 °C and for an immersion time of not longer than 30 minutes. When an ultrasonic cleaning method is used, cleaning must be conducted at a frequency of 48 kHz or lower, at an vibrator output of 0.02 W/cm³, at a temperature of not higher than 40 °C, and for a time of 5 minutes or shorter.

- •Care must be exercised in cleaning process so that the mounted capacitor will not come into contact with any cleaned object or the like or will not get rubbed by a stiff brush or similar. If such precautions are not taken particularly when the ultrasonic cleaning method is employed, terminal breakage may occur.
- When performing ultrasonic cleaning under conditions other than stated above, conduct adequate advance checkout.



Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000