

# 4V Drive Pch MOSFET

# **RSH070P05**

#### Structure

Silicon P-channel MOSFET

#### Features

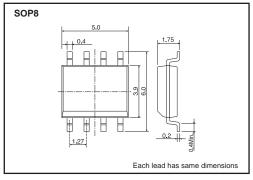
- 1) Built-in G-S Protection Diode.
- 2) Small and Surface Mount Package (SOP8).

#### Application

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Power switching, DC / DC converter, Inverter

#### •Dimensions (Unit : mm)



#### Packaging specifications

	Package	Taping		
Туре	Code	TB		
	Basic ordering unit (pieces)	2500		
RSH070P05		0		

#### ●Absolute maximum ratings (Ta=25°C)

Parameter		Limits	Unit
Drain-source voltage		-45	V
Gate-source voltage		±20	V
Continuous	Ι <sub>D</sub>	±7.0	А
Pulsed	I <sub>DP *1</sub>	±28	А
Continuous	I <sub>S</sub>	-1.6	А
Pulsed	I <sub>SP ∗1</sub>	-28	А
Total power dissipation		2	W
Chanel temperature		150	°C
Range of Storage temperature		-55 to +150	°C
	Continuous Pulsed Continuous Pulsed	Continuous Ip   Pulsed Ip   Pulsed Isp   Pulsed Isp   Pulsed Isp   Tch Tch	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

\*1 PW≤10μs、Duty cycle≤1%

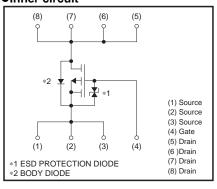
\*2 Mounted on a ceramic board

#### Thermal resistance

Parameter	Symbol	Limits	Unit
Chanel to ambient	R <sub>th(ch-a) *</sub>	62.5	°C/W

\* Mounted on a ceramic board

## Inner circuit



#### •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±10	μΑ	Vgs=±20V, Vds=0V
Drain-source breakdown voltage	V(BR) DSS	-45	-	-	V	I <sub>D</sub> = -1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V <sub>DS</sub> = -45V, V <sub>GS</sub> =0V
Gate threshold voltage	VGS (th)	-1.0	-	-2.5	V	$V_{DS} = -10V, I_{D} = -1mA$
Static drain-source on-state resistance		-	19	27	mΩ	ID= -7A, VGs= -10V
	RDS (on)*	-	25	35	mΩ	I <sub>D</sub> = -7A, V <sub>GS</sub> = -4.5V
Tesistance		_	28	39	mΩ	$I_{D}=-7A, V_{GS}=-4.0V$
Forward transfer admittance	Y <sub>fs</sub> *	10.0	_	-	S	$V_{DS} = -10V, I_D = -7A$
Input capacitance	Ciss	-	4100	-	pF	VDS=-10V
Output capacitance	Coss	-	510	-	рF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	_	330	-	рF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	31	-	ns	Vdd≒-25V
Rise time	tr *	_	35	-	ns	$I_{D} = -3.5A$
Turn-off delay time	t <sub>d (off)</sub> *	_	135	-	ns	Vgs= –10V RL=–7Ω
Fall time	t <sub>f</sub> *	_	50	-	ns	R <sub>g</sub> =10Ω
Total gate charge	Qg *	_	34.0	47.6	nC	V <sub>DD</sub> ≒–25V V <sub>GS</sub> =–5V
Gate-source charge	Q <sub>gs</sub> *	_	9.5	_	nC	ID=-7A
Gate-drain charge	Qgd *	_	12	_	nC	RL=3.5Ω RG=10Ω

### •Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd*	_	-	-1.2	V	I <sub>S</sub> = -7A, V <sub>GS</sub> =0V

\*Pulsed

#### •Electrical characteristic curves

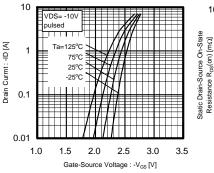


Fig.1 Typical Transfer Characteristics

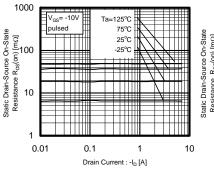


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current (1)

Ta=25°C

ulsed

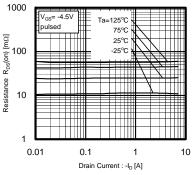


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current (2)

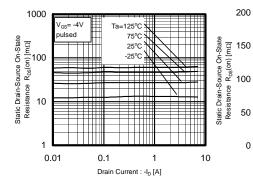
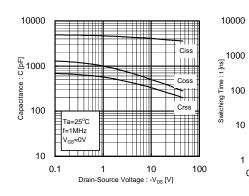


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (3)



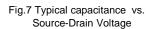


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

Gate-Source Voltage : -V<sub>GS</sub> [V]

10

5

-7.0A

50

0

0

td(off

td(on) 

1

0.01

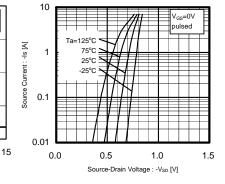


Fig.6 Source-Current vs. Source-Drain Voltage

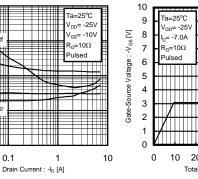
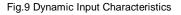


Fig.8 Switching Characteristics

70 20 30 40 50 60 Total Gate Charge : Qg [nC]



#### Measurement circuits

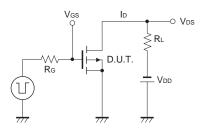


Fig.10 Switching Time Test Circuit

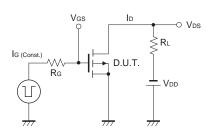


Fig.12 Gate Charge Test Circuit

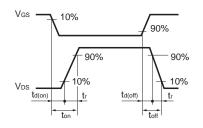


Fig.11 Switching Time Waveforms

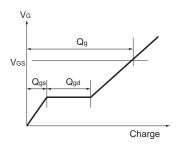


Fig.13 Gate Charge Waveform

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