

# RGTH00TK65D

### 650V 50A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	21A
V <sub>CE(sat) (Typ.)</sub>	1.6V@I <sub>C</sub> =50A
$P_D$	72W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

### Applications

**PFC** 

**UPS** 

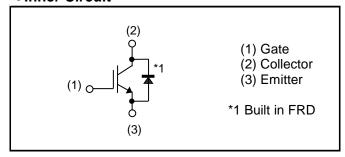
**Power Conditioner** 

ΙH

#### Outline



### ●Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tuno	Tape Width (mm)	-
Type	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGTH00TK65D

### ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit	
Collector - Emitter Voltage	V <sub>CES</sub>	650	V	
Gate - Emitter Voltage		$V_{GES}$	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	35	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	21	А
Pulsed Collector Current	I <sub>CP</sub> *1	200	А	
Diada Famuard Current	T <sub>C</sub> = 25°C	I <sub>F</sub>	34	А
Diode Forward Current	T <sub>C</sub> = 100°C	I <sub>F</sub>	19	А
Diode Pulsed Forward Current	I <sub>FP</sub> *1	200	А	
Dawer Dissination	T <sub>C</sub> = 25°C	P <sub>D</sub>	72	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	36	W
Operating Junction Temperature	T <sub>j</sub>	-40 to +175	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +175	°C	

<sup>\*1</sup> Pulse width limited by  $T_{jmax.}$ 

### ●Thermal Resistance

Parameter	Cymbol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	2.07	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	3.09	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiametei	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C = 10 \mu A, V_{GE} = 0 V$	650	1	-	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	ı	1	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 34.7 \text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C = 50A$ , $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C	-	1.6 2.1	2.1	V

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit			
Farameter	Symbol	ymbol Conditions F		Тур.	Max.	Offic	
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	2740	-		
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$	-	106	-	pF	
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	43	-		
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	94	-		
Gate - Emitter Charge	Q <sub>ge</sub>	I <sub>C</sub> = 50A	-	22	-	nC	
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	31	-		
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 50A, V_{CC} = 400V$	-	39	-		
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	63	-	200	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	143	-	ns	
Fall Time	t <sub>f</sub>	Inductive Load	-	50	-		
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 50A, V_{CC} = 400V$	-	39	-		
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	63	-	200	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	159	-	ns	
Fall Time	t <sub>f</sub>	Inductive Load	-	62	-		
		$I_C = 200A, V_{CC} = 520V$					
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 \text{V}, \ V_{GE} = 15 \text{V}$	FU	LL SQUA	RE	-	
		$R_G = 60\Omega, T_j = 175^{\circ}C$					

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Darameter	Symbol	Conditions	Values			l lm:t
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>F</sub>	$I_F = 30A$ $T_j = 25$ °C $T_j = 175$ °C	-	1.45 1.25	2.0	V
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30A	-	54	ı	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	7.4	-	А
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.22	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30A	-	225	ı	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	V <sub>CC</sub> = 400V di <sub>F</sub> /dt = 200A/μs	-	12.8	1	А
Diode Reverse Recovery Charge	$Q_{rr}$	T <sub>j</sub> = 175°C	-	1.60	-	μC

Fig.1 Power Dissipation vs. Case Temperature

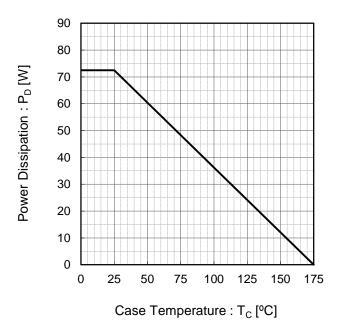


Fig.2 Collector Current vs. Case Temperature

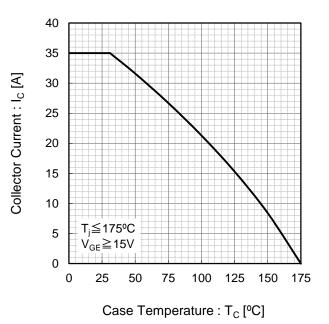
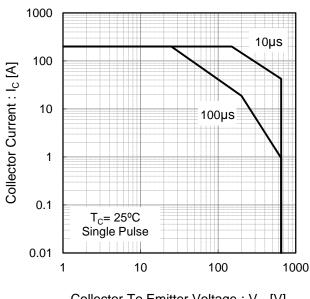


Fig.3 Forward Bias Safe Operating Area



Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.4 Reverse Bias Safe Operating Area

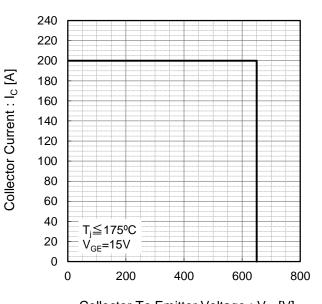


Fig.5 Typical Output Characteristics

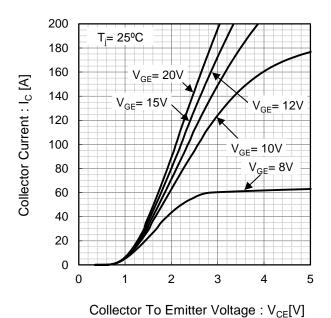
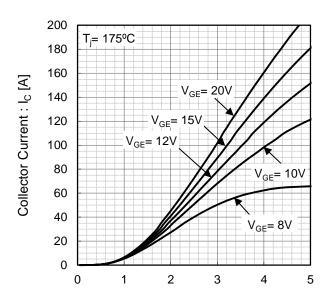


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V<sub>CE</sub>[V]

Fig.7 Typical Transfer Characteristics

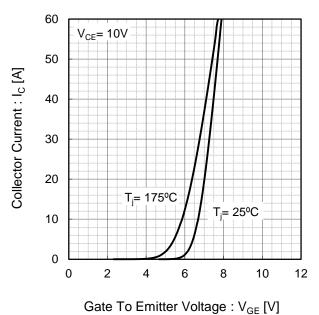
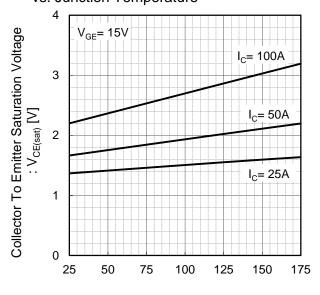


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

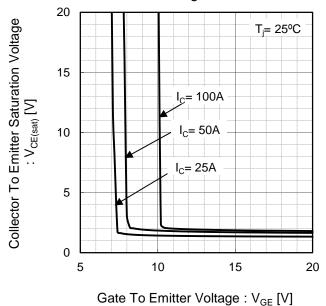
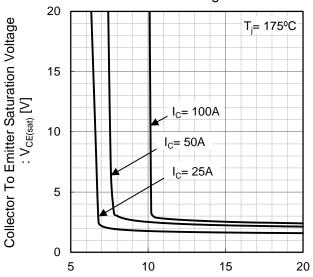


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

1000

t<sub>d(off)</sub>

V<sub>cc</sub>=400V, V<sub>GE</sub>=15V
R<sub>G</sub>=10Ω, T<sub>j</sub>=175°C
Inductive load

0 10 20 30 40 50 60 70 80 90 100

Collector Current : I<sub>C</sub> [A]

Fig.12 Typical Switching Time vs. Gate Resistance 1000 Switching Time [ns] 100 t<sub>d(on)</sub>  $V_{CC} = 400V, I_{C} = 50A$ V<sub>GE</sub>=15V, T<sub>i</sub>=175°C Inductive load 10 10 40 0 20 30 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.13 Typical Switching Energy Losses vs. Collector Current

10  $E_{on}$ 0.1  $E_{on}$   $V_{cc}=400V, V_{gE}=15V$   $E_{gE}=100, T_{j}=175^{\circ}C$ Inductive load

0 10 20 30 40 50 60 70 80 90 100

Collector Current :  $I_{c}$  [A]

Fig.14 Typical Switching Energy Losses vs. Gate Resistance 10 Switching Energy Losses [mJ]  $\mathsf{E}_{\mathsf{off}}$ 1  $\mathsf{E}_{\mathsf{on}}$ 0.1  $V_{CC}$ =400V,  $I_{C}$ =50A  $V_{GE}$ =15V,  $T_{j}$ =175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz  $V_{GE}=0V$ T;=25°C 0.01 0.1 10 100 Collector To Emitter Voltage : V<sub>CE</sub>[V]

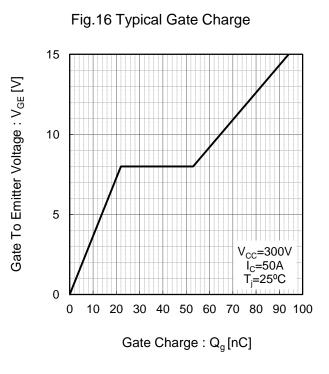


Fig.17 Typical Diode Forward Current vs. Forward Voltage 200 180 160 Forward Current : I<sub>F</sub> [A] 140 120 100 80 60 T<sub>i</sub>= 175°C 40 T<sub>i</sub>= 25°C 20 0 3.5 0 0.5 1.5 2 2.5 3 Forward Voltage : V<sub>F</sub>[V]

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400  $V_{CC}$ =400V di<sub>F</sub>/dt=200A/µs Reverse Recovery Time: t<sub>rr</sub> [ns] Inductive load 300 T<sub>i</sub>= 175°C 200 100 T<sub>i</sub>= 25°C 0 10 20 30 40 50 0 Forward Current : I<sub>F</sub> [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

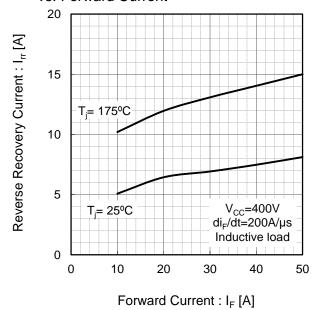
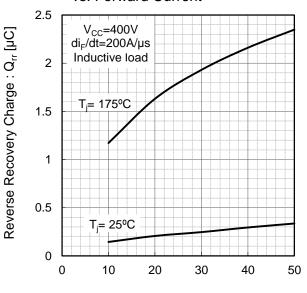


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



Forward Current : I<sub>F</sub> [A]

Fig.21 IGBT Transient Thermal Impedance

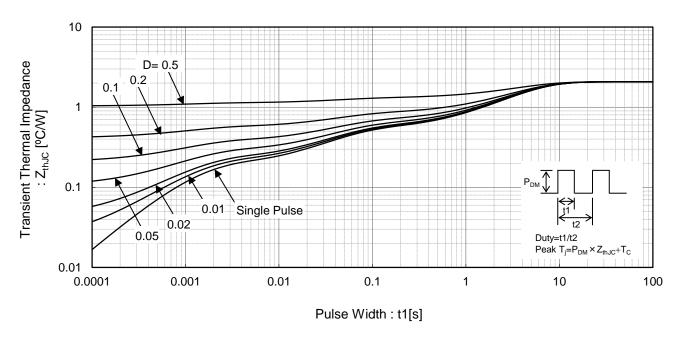
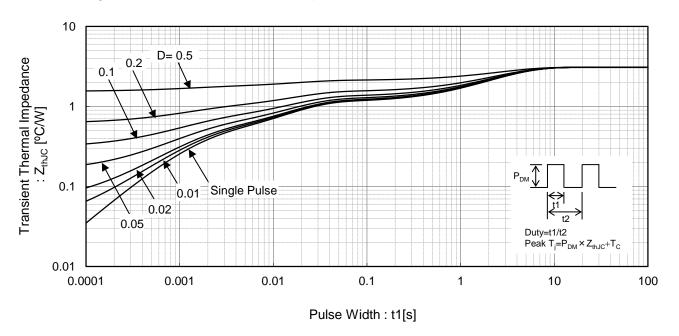


Fig.22 Diode Transient Thermal Impedance



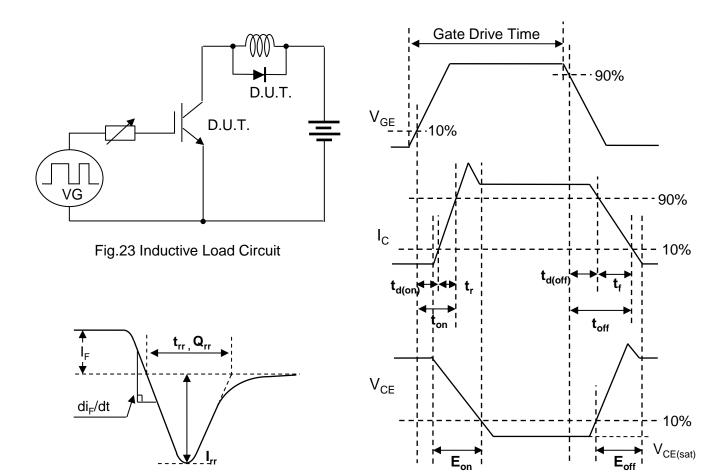


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

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# RGTH00TK65D - Web Page

**Distribution Inventory** 

Part Number	RGTH00TK65D
Package	TO-3PFM
Unit Quantity	450
Minimum Package Quantity	450
Packing Type	Tube
Constitution Materials List	inquiry
RoHS	Yes