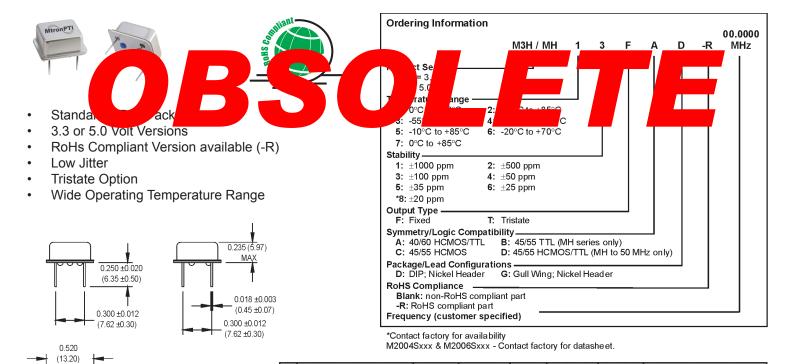
M3H & MH Series

8 pin DIP, 3.3 or 5.0 Volt, HCMOS/TTL Clock Oscillator





Pin Connections

INSULATED STANDOFFS

0.520

(13.20)MAX

All dimensions in inches (mm).

MAX

0

PIN	FUNCTION				
1	N/C or Tristate				
4	Circuit/Case Ground				
5	Output				
8	+Vdd				

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes	
	Frequency Range	F	1.5		100	MHz	M3H See Note 1	
			1.0		80	MHz	MH	
	Operating Temperature	TA	(See ordering information)					
	Storage Temperature	Ts	-55		+125	°C		
	Frequency Stability	ΔF/F	(See ordering information)					
Specifications	Aging 1st Year Thereafter (per year)			±3 ±2		ppm ppm		
	Input Voltage	Vdd	3.135	3.3	3.465	V	МЗН	
			4.5	5.0	5.5	V	MH	
	Input Current (M3H)	ldd			25 35 55	mA mA mA	1.5000 to 50.000 MHz 50.001 to 67.000 MHz 67.001 to 100.000 MHz	
	Input Current (MH)	ldd			40 60	mA mA	1.000 to 40.000 MHz 40.001 to 80.000 MHz	
<u>a</u>	Output Type						HCMOS/TTL	
Electrical	Load			10 TTI	or 15 pF _ or 50 pF	M3H See Note 2		
ă	Symmetry (Duty Cycle)		(See ordering information)				See Note 3	
	Logic "1" Level	Voh	90% Vdd Vdd-0.5			V V	HCMOS Load TTL Load	
	Logic "0" Level	Vol			10% Vdd 0.5	V V	HCMOS Load TTL Load	
	Output Current				±4 ±16	mA mA	M3H MH	
	Rise/Fall Time	Tr/Tf			10	ns	See Note 4	
	Tristate Function		Input Logic "1" or floating: output active Input Logic "0": output disables to high-Z					
	Start up Time				10	ms		
	Random Jitter	Rj		5	12	ps RMS	1-Sigma	
Environmental	Markania I Charl		MIL CTD 202	Madhad	242 0 (400	w/a)		
	Mechanical Shock		MIL-STD-202, Method 213, C (100 g/s)					
	Vibration		MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
	Thermal Cycle		MIL-STD-883, Method 1010, B (-55°C to +125°C, 15 min dwell, 10 cycles) MIL-STD-202, Method 112					
] ≥	Hermeticity		,					
ш	Solderability		Per EIAJ-STD-002					
ļ	Max Wave Soldering Conditions +260°C for 10 seconds 1. Contact the factory for availability of higher frequencies							

- Contact the factory for availability of higher frequencies.
 TTL load see Load Circuit Diagram #1. HCMOS load see Load Circuit Diagram #2.
 Symmetry is measured at 1.4 V with TTL load and at 50% Vdd with HCMOS load.
- 4. Rise/fall times are measured between 0.4 V and 2.4 V with TTL load, and between 10% Vdd and 90% Vdd with HCMOS Load.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.