

# FAST RECOVERY GLASS PASSIVATED RECTIFIER

VOLTAGE RANGE 50 to 1000 Volts CURRENT 3.0 Amperes

## FEATURES

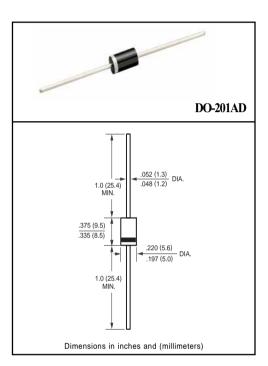
- \* High reliability
- \* Low leakage
- \* Low forward voltage drop
- \* High current capability
- \* Glass passivated junction
- \* High switching capability

### **MECHANICAL DATA**

- \* Case: Molded plastic
- \* Epoxy: Device has UL flammability classification 94V-O
- \* Lead: MIL-STD-202E method 208C guaranteed
- \* Mounting position: Any
- \* Weight: 1.18 grams

### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.



#### MAXIMUM RATINGS (At TA = 25°C unless otherwise noted)

RATINGS	SYMBOL	FR301G	FR302G	FR303G	FR304G	FR305G	FR306G	FR307G	UNITS
Maximum Recurrent Peak Reverse Voltage	Vrrm	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	Vrms	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TA = 55°C	Io	3.0							Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	IFSM	125						Amps	
Typical Junction Capacitance (Note 2)	CJ	50							рF
Operating and Storage Temperature Range	TJ, TSTG	-65 to + 150							°C

#### ELECTRICAL CHARACTERISTICS (At TA = 25°C unless otherwise noted)

CHARACTERISTICS	SYMBOL	FR301G FR302G	FR303G	FR304G	FR305G	FR306G	FR307G	UNITS
Maximum Instantaneous Forward Voltage at 3.0A DC	VF	1.3						Volts
Maximum DC Reverse Current at Rated DC Blocking Voltage TA = 25°C	-	5.0						
Maximum Full Load Reverse Current Average, Full Cycle .375" (9.5mm) lead length at T∟ = 55°C	- IR	100						uAmps
Maximum Reverse Recovery Time (Note 1)	trr	15	50		250	50	00	nSec

NOTES : 1. Test Conditions: IF = 0.5A, IR = -1.0A, IRR = -0.25A

Measured at 1 MHz and applied reverse voltage of 4.0 volts

## RATING AND CHARACTERISTIC CURVES (FR301G THRU FR307G)

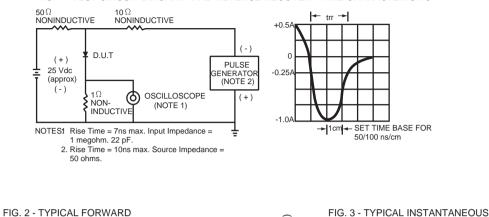
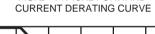


FIG. 1 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



3.0

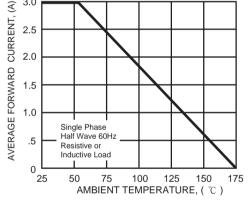


FIG. 4 - TYPICAL JUNCTION CAPACITANCE

