F98-AS1 Series

Fused Face-Down, High CV





FEATURES

• Compliant to the RoHS2 directive 2011/65/EU



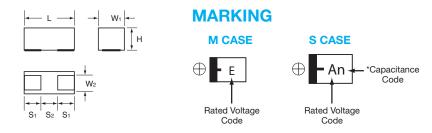


APPLICATIONS

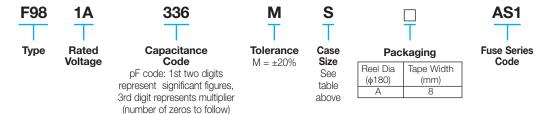
- Industrial equipment
- Smartphone
- Medical equipment
- Automotive electronics
- Portable game

CASE DIMENSIONS: millimeters (inches)

Code	EIA Code	EIA Metric	L	W ₁	W ₂	н	S ₁	W ₂
M	0603	1608-09	1.60 ^{+0.20} _{-0.10} (0.063 ^{+0.008} _{-0.004})	0.85 ^{+0.20} _{-0.10} (0.033 ^{+0.008} _{-0.004})	0.65±0.10 (0.026±0.004)	0.80±0.10 (0.031±0.004)	0.50±0.10 (0.020±0.004)	0.60±0.10 (0.024±0.004)
s	0805	2012-09	2.00 ^{+0.20} _{-0.10} (0.079 ^{+0.008} _{-0.004})	1.25 ^{+0.20} _{-0.10} (0.049 ^{+0.008} _{-0.004})	0.90±0.10 (0.035±0.004)	0.80±0.10 (0.031±0.004)	0.50±0.10 (0.020±0.004)	1.00±0.10 (0.039±0.004)



HOW TO ORDER



TECHNICAL SPECIFICATIONS

Category Temperature Range:	-55 to +125°C					
Rated Temperature:	+85°C					
Capacitance Tolerance:	±20% at 120Hz					
Dissipation Factor:	Refer to next page					
ESR 100kHz:	Refer to next page					
Leakage Current:	Refer to next page					
	Provided that:					
	After 5 minute's application of rated voltage, leakage current at 85°C					
	10 times or less than 20°C specified value.					
	After 5 minute's application of rated voltage, leakage current at 125°C					
	12.5 times or less than 20°C specified value.					

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CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance		*Cap				
μF Code		10V (1A)	16V (1C)	20V (1D)	25V (1E)	35 (1V)	Code
1.0	105		M*	M*	M*	S	А
2.2	225	M*	M*				_
4.7	475	M*	M*				-
10	106	M*	S				a
22	226	M*/S					J
33	336	M*/S					n
47	476	S					_

Available Ratings

Please contact to your local AVX sales office when these series are being designed in your application.

RATINGS & PART NUMBER REFERENCE

AVX Part No.	Case Size	Capacitance (µF)	Rated Voltage (V)	*2 DCL (μΑ)	DF @ 120Hz (%)	ESR @ 100kHz (Ω)	*1 △C/C (%)
	10 Volt						
F981A226MSAAS1	S	22	10	2.2	20	4.5	±20
F981A336MSAAS1	S	33	10	3.3	30	6.5	±30
F981A476MSAAS1	S	47	10	9.4	35	5.5	±30
16 Volt							
F981C106MSAAS1	S	10	16	1.6	18	4.5	±20
35 Volt							
F981V105MSAAS1	S	1	35	0.7	20	8.5	±30

*2: Leakage Current After 5 minute's application of rated voltage, leakage current at 20°C.

QUALIFICATION TABLE

TEST	F98-AS1 series (Temperature range -55°C to +125°C)						
1231	Condition						
	At 40°C, 90 to 95% R.H., 500 hours (No voltage applied)						
Damp Heat	Capacitance Change Refer to the table above (*1)						
Steady State)	Dissipation Factor						
	Leakage Current						
	-55°C / +125°C, 30 minutes each, 5 cycles						
Temperature Cycles	Capacitance Change Refer to the table above (*1)						
remperature cycles	Dissipation Factor						
	Leakage Current						
	10 seconds reflow at 260°C, 5 seconds immersion at 260°C.						
Resistance to	Capacitance Change Refer to the table above (*1)						
Soldering Heat	Dissipation Factor Initial specified value or less						
	Leakage Current Initial specified value or less						
	After application of surge in series with a 1kΩ resistor at the rate of 30 seconds ON, 30 seconds OFF,						
	for 1000 successive test cycles at 85°C, capacitors shall meet the characteristic requirements in the table above.						
Surge	Capacitance Change Refer the table above (*1)						
	Dissipation Factor						
	Leakage Current						
	After 1000 hours' application of rated voltage in series with a 3Ω resistor at 85°C,						
	capacitors shall meet the characteristic requirements in the table above.						
Endurance	Capacitance Change Refer to the table above (*1)						
	Dissipation Factor						
	Leakage Current						
a	After applying the pressure load of 5N for 10±1 seconds horizontally to the center of capacitor side body						
Shear Test	which has no electrode and has been soldered beforehand on a substrate, there shall be found neither 5N (0.51kg·1)						
	exfoliation nor its sign at the terminal electrode.						
	Keeping a capacitor surface-mounted on a substrate upside down and supporting the substrate at						
Terminal Strength	both of the opposite bottom points 45min apart from the center of capacitor, the pressure strength is						
	applied with a specified jig at the center of substrate so that the substrate may bend by 1mm as						
	illustrated. Then, there shall be found no remarkable abnormality on the capacitor terminals.						
Fuse activation	5 seconds max. with 2A min. applied current						

NOTICE: DESIGN, SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.

^{*}Codes under development – subject to change