



DMN2053UWQ

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
20V	56mΩ @ Vgs = 4.5V	2.9A
	65mΩ @ V _{GS} = 2.5V	2.7A
	93mΩ @ Vgs = 1.8V	2.2A
	140mΩ @ V _{GS} = 1.5V	1.8A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN2053UWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

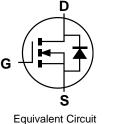
Mechanical Data

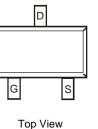
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.027 grams (Approximate)



SOT323

Top View





Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2053UWQ-7	SOT323	3,000/Tape & Reel
DMN2053UWQ-13	SOT323	10,000/Tape & Reel

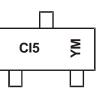
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:



CI5 = Product Type Marking Code

- YM = Date Code Marking
- \overline{Y} = Year (ex: H = 2020)
- M = Month (ex: 9 = September)

Date Code Key												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н	I	J	K	L	М	N	0	Р	R	S
	1											
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	4	0	0	4	5	6	7	8	q	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	Vdss	20	V
Gate-Source Voltage	V _{GSS}	±12	V
Continuous Drain Current (Note 6) V_{GS} = 4.5V	ID	2.9 2.3	А
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)	ldм	20	A
Maximum Body Diode Forward Current (Note 5)	ls	1.0	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.47	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	268	°C/W
Total Power Dissipation (Note 6)		PD	0.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	178	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current @Tc = +25°C	IDSS	-	—	1	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	_	±1	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.35		1.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			39	56		$V_{GS} = 4.5V, I_{D} = 2A$
Static Drain-Source On-Resistance	Descent	_	45	65	mΩ	VGS = 2.5V, ID = 2A
	RDS(ON)		51	93	11152	$V_{GS} = 1.8V, I_D = 1A$
			75	140		$V_{GS} = 1.5V, I_{D} = 0.5A$
Diode Forward Voltage	Vsd		0.7	1.0	V	$V_{GS} = 0V$, $I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	369		pF	
Output Capacitance	Coss	_	54		pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	32		pF	1 - 1.00012
Gate Resistance	Rg		4.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg	_	3.6	_	nC	
Gate-Source Charge	Qgs	_	0.4	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_{D} = 6A$
Gate-Drain Charge	Qgd	_	1.0	_	nC	
Turn-On Delay Time	tD(ON)	_	2.6	_	ns	
Turn-On Rise Time	tR	_	3.0	_	ns	V _{DD} = 10V, V _{GS} = 5V,
Turn-Off Delay Time	tD(OFF)		12.5		ns	$R_G = 6\Omega, I_D = 6A$
Turn-Off Fall Time	tF		3.6		ns]
Reverse Recovery Time	t _{RR}		6.0	_	ns	IF = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	0.9		nC	I _F = 1.0A, di/dt = 100A/µs

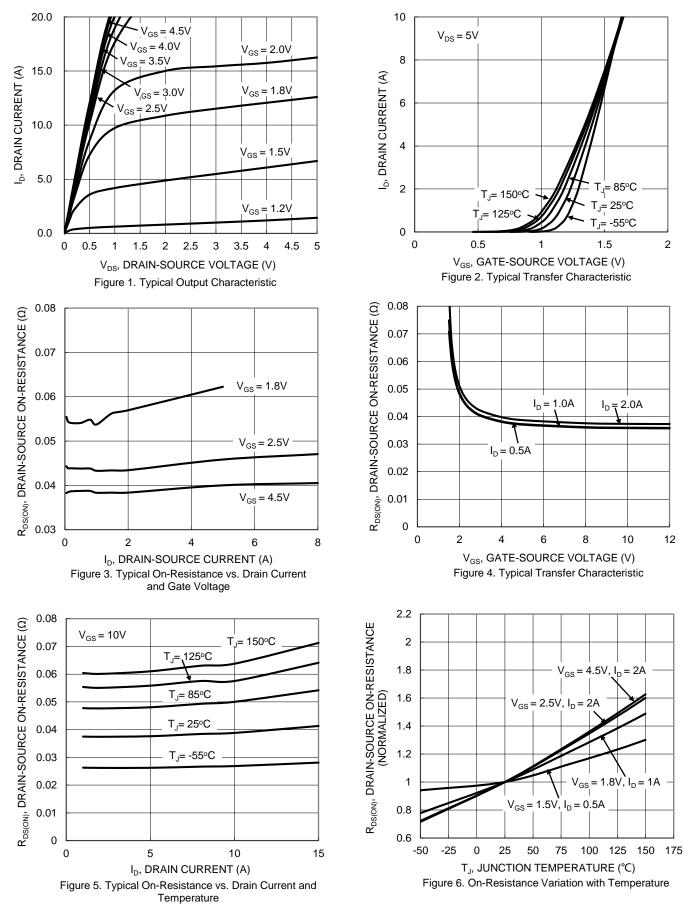
Notes:

5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

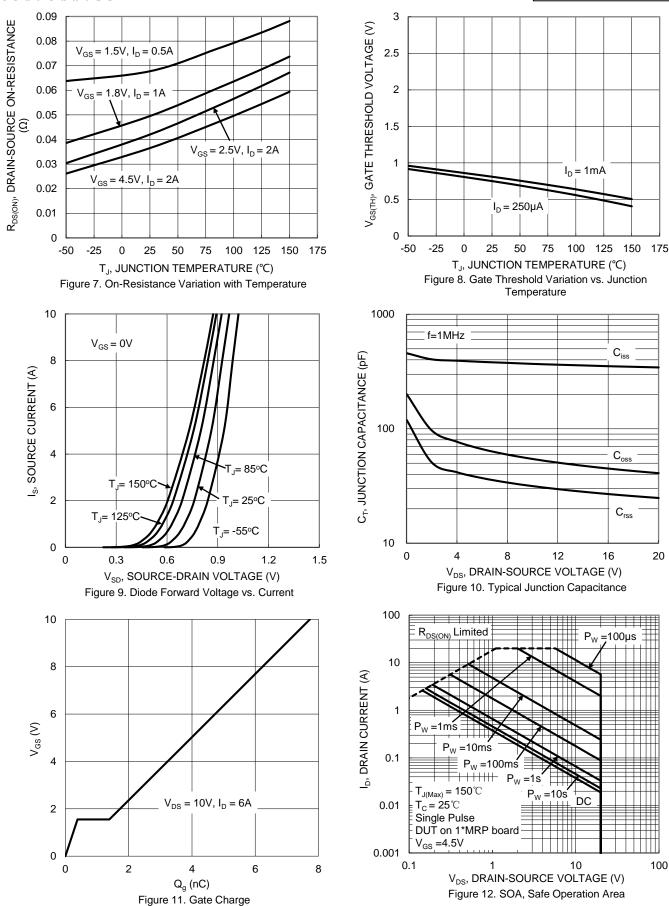


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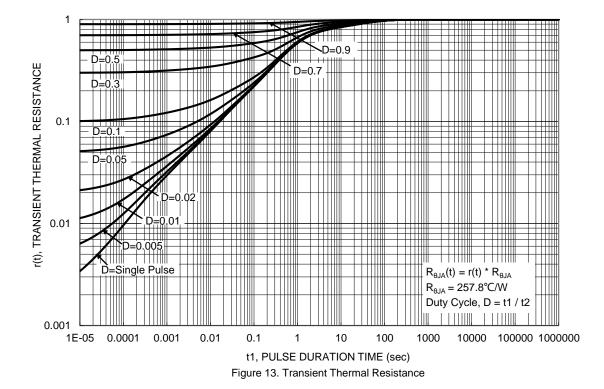




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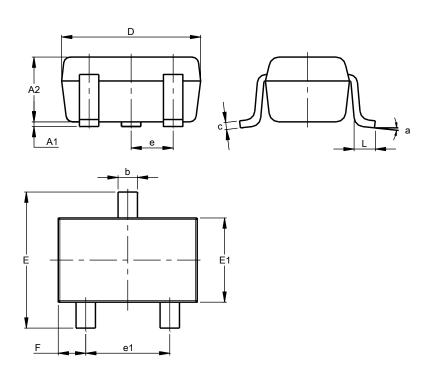






Package Outline Dimensions

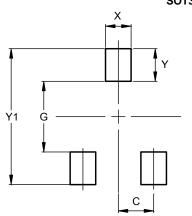
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323									
Dim	Min Max Typ								
A1	0.00	0.10	0.05						
A2	0.90	1.00	0.95						
b	0.25	0.40	0.30						
С	0.10	0.18	0.11						
D	1.80	2.20	2.15						
Е	2.00	2.20	2.10						
E1	1.15	1.35	1.30						
е	C).650 B	SC						
e1	1.20	1.40	1.30						
F	0.375	0.475	0.425						
L	0.25	0.40	0.30						
а	0°	8°							
All Dimensions in mm									

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323

Dimensions	Value (in mm)				
С	0.650				
G	1.300				
Х	0.470				
Y	0.600				
Y1	2.500				



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