



DMN2024UQ

### **Product Summary**

BV <sub>DSS</sub>	Rds(on) Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	25mΩ @ V <sub>GS</sub> = 4.5V	6.8A
20V	29mΩ @ V <sub>GS</sub> = 2.5V	5.5A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) yet maintain superior switching performance, which make it ideal for high-efficiency power management applications.

- Backlighting
- Power-Management Functions
- DC-DC Converters
- Motor Control
  - ESD-Protected Gate



Top View

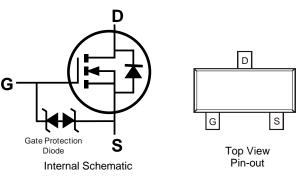
## N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Features and Benefits**

- Low On-Resistance
- Low-Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN2024UQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ©3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2024UQ-7	SOT23	3,000/Tape & Reel
DMN2024UQ-13	SOT23	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

05U	ΥM

05U = Product Type Marking CodeYM = Date Code Marking $Y or <math>\overline{Y}$  = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

•												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	Ν	0	Р	R	S
r												
				-		_			•	• •		-
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		Vdss	20	V	
Gate-Source Voltage			V <sub>GSS</sub>	±10	V
	Steady	T <sub>A</sub> = +25°C	1-	6.8	٨
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	State	T <sub>A</sub> = +70°C	ID	5.5	A
Maximum Continuous Body Diode Forward Curre	ent (Note 6)	ls	2.2	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)		I <sub>DM</sub>	45	А

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Rəja	159	°C/W
Total Power Dissipation (Note 6)		PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Rəja	92	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

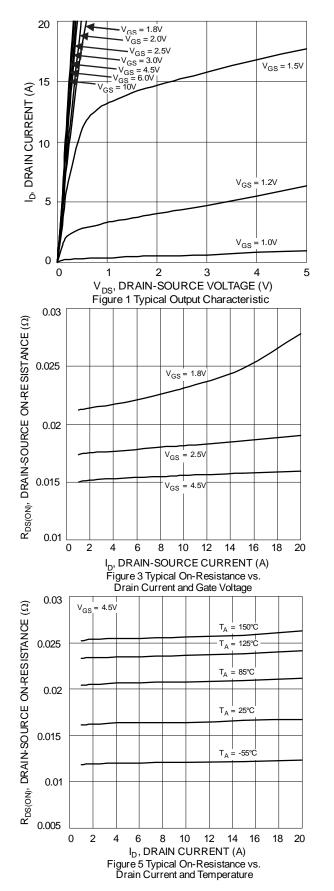
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						•
Drain-Source Breakdown Voltage	BVDSS	20		_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS			1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	lgss		-	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)			-			
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5		0.9	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
			16	25		$V_{GS} = 4.5V, I_D = 6.5A$
Static Drain-Source On-Resistance	RDS(ON)	—	18.5	29	mΩ	$V_{GS} = 2.5V, I_{D} = 5.5A$
			23	36		VGS = 1.8V, ID = 3.5A
Diode Forward Voltage	Vsd	—	0.8	1.2	V	$V_{GS} = 0V, I_D = 5A$
DYNAMIC CHARACTERISTICS (Note 8)					-	
Input Capacitance	Ciss	—	647	—	pF	
Output Capacitance	Coss		78	_	pF	VDS = 10V, VGS = 0V −f = 1.0MHz
Reverse Transfer Capacitance	Crss		38	—	pF	
Gate Resistance	Rg		400	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg		6.5	_	nC	
Gate-Source Charge	Qgs		1.1	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$
Gate-Drain Charge	Q <sub>gd</sub>		1.7	-	nC	
Turn-On Delay Time	tD(ON)	_	98	—	ns	
Turn-On Rise Time	t <sub>R</sub>		140	—	ns	$V_{DS} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)		1024	_	ns	$R_L = 10\Omega, \ R_g = 6\Omega, \ I_D = 1A$
Turn-Off Fall Time	tF		434	_	ns	
Reverse Recovery Time	trr		245	—	ns	IF = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Qrr		149	—	nC	IF = 1.0A, di/dt = 100A/µs

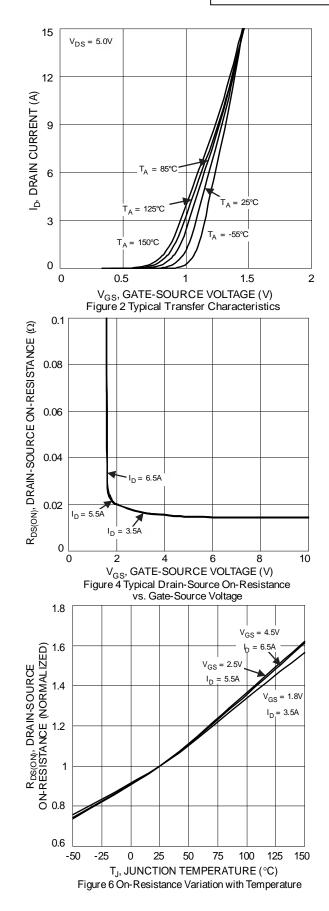
Notes:

Device mounted on FR-4 PCB with minimum recommended pad layout.
 Device mounted on 1" x 1" FR-4 PCB with high-coverage 2oz copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

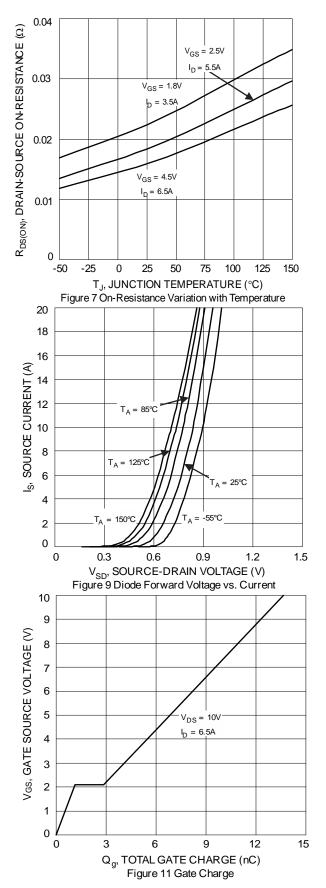


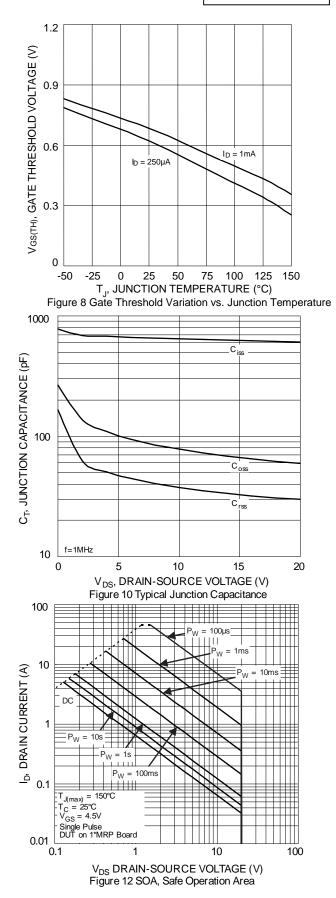
## DMN2024UQ



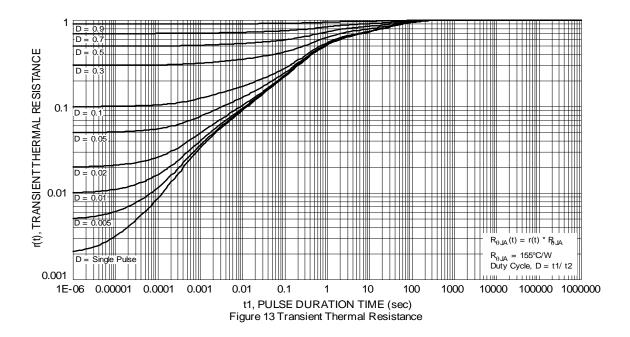








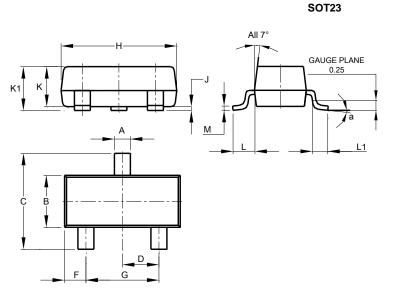






### **Package Outline Dimensions**

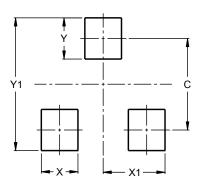
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
К	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

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