

Super Low Distortion Multilayer Ceramic Capacitors (CFCAP™)

UMK212SD103KD-T



■ Features

- Item Summary
0.01 uF±10%, 50V,
CFCAP, 0805/2012 (EIA/JIS)
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 4000pcs

■ Products characteristics table

Capacitance	0.01 uF ± 10 %
Case Size (EIA/JIS)	0805/2012
Rated Voltage	50 V
tan δ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
HALT (% Rated Voltage)	200 %
Insulation Resistance (min)	10 GΩ
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (163 subst.)	Yes
Soldering	Reflow

■ External Dimensions

Dimension L	2.0 ±0.10 mm
Dimension W	1.25 ±0.10 mm
Dimension T	0.85 ±0.10 mm
Dimension e	0.50 ±0.25 mm

2016.02.14

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

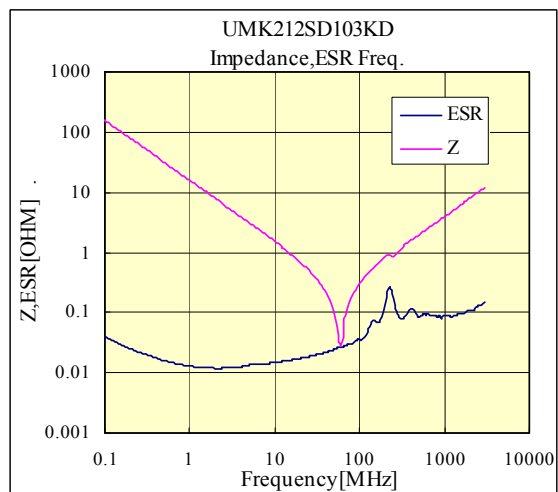
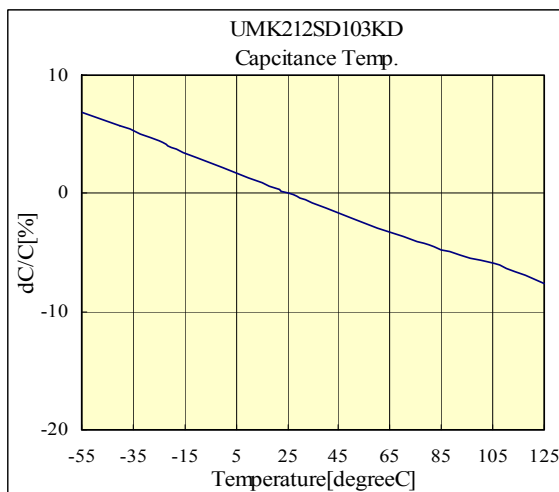
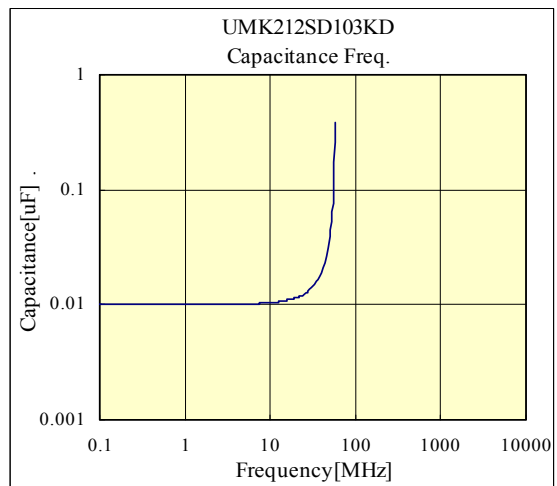
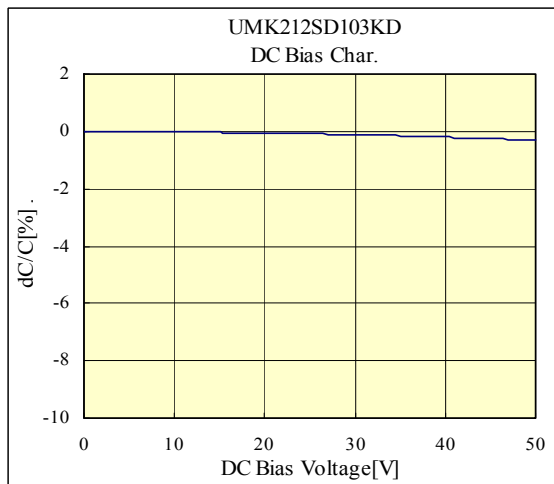
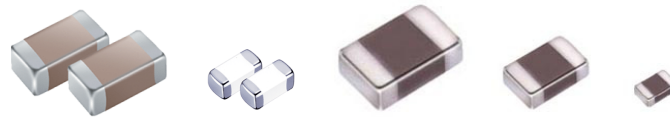
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-Electrical Characteristics Data- Jun-2011

RoHS compliance product

○UMK212SD103KD

50V 2012 0.01 μ F +/-10% t=0.95mmMAX
50V 0805 0.01 μ F +/-10% t=0.037inchMAX



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