## ARM Serlis

Integrated Duplex Controller with Intrinsically Safe Inputs


The ARM Series Alternating Relay is a microprocessor based controller designed for use in dual load installations to assure equal run time on each load. LDD indicators show the status of the unit's five intrinsically safe control switch inputs, one alarm, and two load outputs. H-O-A switches, a lead select switch, and a test/clear button are provided for manual control. The ARM Series reduces the number of components required for this application by combining many functions into one unit.

## FEATURES

$\star$ TWO PUMP SEQUENCING
Evenly distributes run time by automatically alternating lead and lag load designations when the off control switch input opens
$\star$ UL913 INTRINSICALLY SAFE
Control switch inputs are low voltagellow current and are electronically isolated from the control voltage and load-alarm contacts.

* H-O-A SWITCHES

Hand-of-automatic switches allow for manual operation.
$\star$ LEAD SELECT SWITCH
Disables the automatic sequencing function and allows loads to be locked into the 2-1 or 1-2 sequence.
$\star$ CONTROL SWITCH FAULT DETECTION
Unit detects open and shorted control switch failures.
$\star$ TEST/CLEAR SWITCH
Verifies function and resets the control switch fault detection algorithm.
$\star$ ALARM OUTPUT
Alarm contacts close when a control switch fails or the system's capacity is exceeded.

* INRUSH CURRENT DELAY

Reduces line sags by preventing both loads from energizing simultaneously.
$\star$ VERSATILE MOUNTING
Two (2) mounting configurations are available. The standard surface mount has top access to controls and indicators and is intended for back panel mounting. The panel mount option is intended for front panel or door cutout access to controls and indicators.

* SPECIAL CONTROLS

ARM-2003 and ARM-2010:
Standard operation without the HOA switches ARM-2011:

Standard operation without the Control Switch Failure feature.

ORDER INFORMATION ARM-120-AFE

| Part \# | Control Voltage | Mounting | Comments |
| :--- | :---: | :---: | :---: |
| ARM-XXX-AFE* | 24 or 120 VAC | Surface | Standard |
| ARM-XXX-AFEP* $^{\star}$ | 24 or 120 VAC | Panel | Standard |
| ARM-2003 | 120 VAC | Surface | Special: w/o HOA switches |
| ARM-2010 | 120 VAC | Panel | Special: w/o HOA switches |
| ARM-2011 | 120 VAC | Surface | Special: w/o Control <br> switch failure feature |

* Replace XXX with desire control voltage $(24,120)$


## OPERATION <br> FOUR CONTROL SWITCHING

Do not remove factory-installed jumper between terminals 2 and 3 . The control switches connected to terminals 3 through 6 are labeled OF (cs2), LEAD (cs3), LAG (cs4) and ALARM (cs5). Under normal operation the lead load energizes when the off and lead control switches close in order. The lag load energizes when the lag closes and the alarm load energizes when the alarm switch closes. When all four switches reopen in the proper order all outputs are de-energized and the lead/lag output designations reverse.

## FIVE CONTROL SWITCHING

Remove factory installed jumper between terminals 2 and 3 . After the jumper has been removed, the additional control switch is connected to terminal 2. The extra switch functions as an AUXILIARY OF (cs1) switch. It is used to prevent loads from running continuously if the primary OF (cs2) switch fails to open properly.

## FAULT DETECTION ALGORITHM

If any of the control switches open or closeout of order, thealarm output energizes and a fault detection algorithm is used to identify the faulty switch. The faulty switch is then ignored and the OF, LEAD, and LAG control switch designations are altered to maintain safe operation.

## DIMENSIONS <br> INCHES



* GRETVLEIPUNCH \#60071 OREQUIVALENT


## CONTROL DRAWING 190



